

CPLD MAX® 3000A Family 2.5K Gates 128 Macro Cells 98MHz 3.3V 100-Pin TQFP Tray

Manufacturers	Altera Corporation (Intel)
Package/Case	TQFP-100
Product Type	Programmable Logic ICs
RoHS	
Lifecycle	



Images are for reference only

Please submit RFQ for EPM3128ATI100-10N or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

[RFQ](#)

General Description

EPM3128ATI100-10N is a programmable logic device (PLD) manufactured by Intel Corporation (formerly Altera Corporation).

Features

It has a capacity of 128 macrocells, which are the basic building blocks of programmable logic devices.

It operates at a maximum frequency of 100 MHz.

It has a 10 ns maximum propagation delay time.

It uses the MAX 7000 architecture, which provides high performance and flexibility in implementing digital circuits.

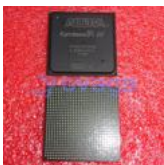
Application

EPM3128ATI100-10N can be used in a wide range of applications, including digital signal processing, communication systems, computer peripherals, and control systems.

It can be used to implement complex digital functions such as state machines, counters, and data path logic.

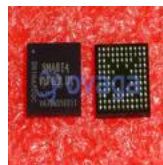


Related Products



[EP4CE55F29C8N](#)

Altera Corporation (Intel)
FBGA-780



[EPM240M100C5N](#)

Altera Corporation (Intel)
BGA-100



[EPM1270T144A5N](#)

Altera Corporation (Intel)
TQFP-144



[EPM570F256C5N](#)

Altera Corporation (Intel)
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[EPM7128AETC100-10](#)

Altera Corporation (Intel)
TQFP-100



[EP2C35F484C7N](#)

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FBGA-484