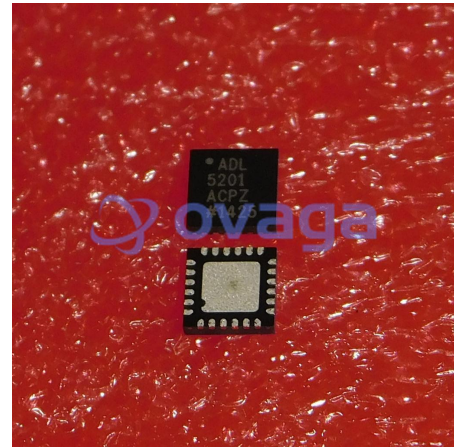


RF Amplifier 31.5 dB Range 0.5 dB Step Size VGA

Manufacturers	Analog Devices, Inc
Package/Case	LFCSP24
Product Type	RF Amplifier
RoHS	Rohs
Lifecycle	



Images are for reference only

Please submit RFQ for ADL5201ACPZ-R7 or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

[RFQ](#)

General Description

The ADL5201 is a digitally controlled, variable gain, wide band-width amplifier that provides precise gain control, high IP3, and low noise figure. The excellent distortion performance and high signal bandwidth make the ADL5201 an excellent gain control device for a variety of receiver applications. The ADL5201 also incorporates a low power mode option that lowers the supply current.

For wide input dynamic range applications, the ADL5201 provides a broad 31.5 dB gain range with 0.5 dB resolution. The gain is adjustable through multiple gain control interface options: parallel, serial peripheral interface, and up/down.

Incorporating proprietary distortion cancellation techniques, the ADL5201 achieves an output IP3 of greater than 47 dBm at frequencies approaching 200 MHz for most gain settings.

The ADL5201 is powered on by applying the appropriate logic level to the PWUP pin. The quiescent current of the ADL5201 is typically 80 mA in low power mode. When configured in high performance mode for more demanding applications, the quiescent current is 110 mA. When powered down, the ADL5201 consumes less than 7 mA and offers excellent input-to-output isolation. The gain setting is preserved during power-down.

Fabricated on an Analog Devices, Inc., high speed SiGe process, the ADL5201 provides precise gain adjustment capabilities with good distortion performance and low phase error. The ADL5201 amplifier comes in a compact, thermally enhanced, 24-lead, 4 mm × 4 mm LFCSP package and operates over the temperature range of -40°C to +85°C.

Features

0.5 dB \pm 0.1 dB step size

150 Ω differential input and output

7.5 dB noise figure at maximum gain

OIP3 > 50 dBm at 200 MHz

Wide input dynamic range

Multiple control interface options Parallel 6-bit control interface (with latch) Serial peripheral interface (SPI) (with fast attack) Gain up/down mode

Low power mode option

Power-down control

Single 5 V supply operation

24-lead LFCSP 4 mm x 4 mm package

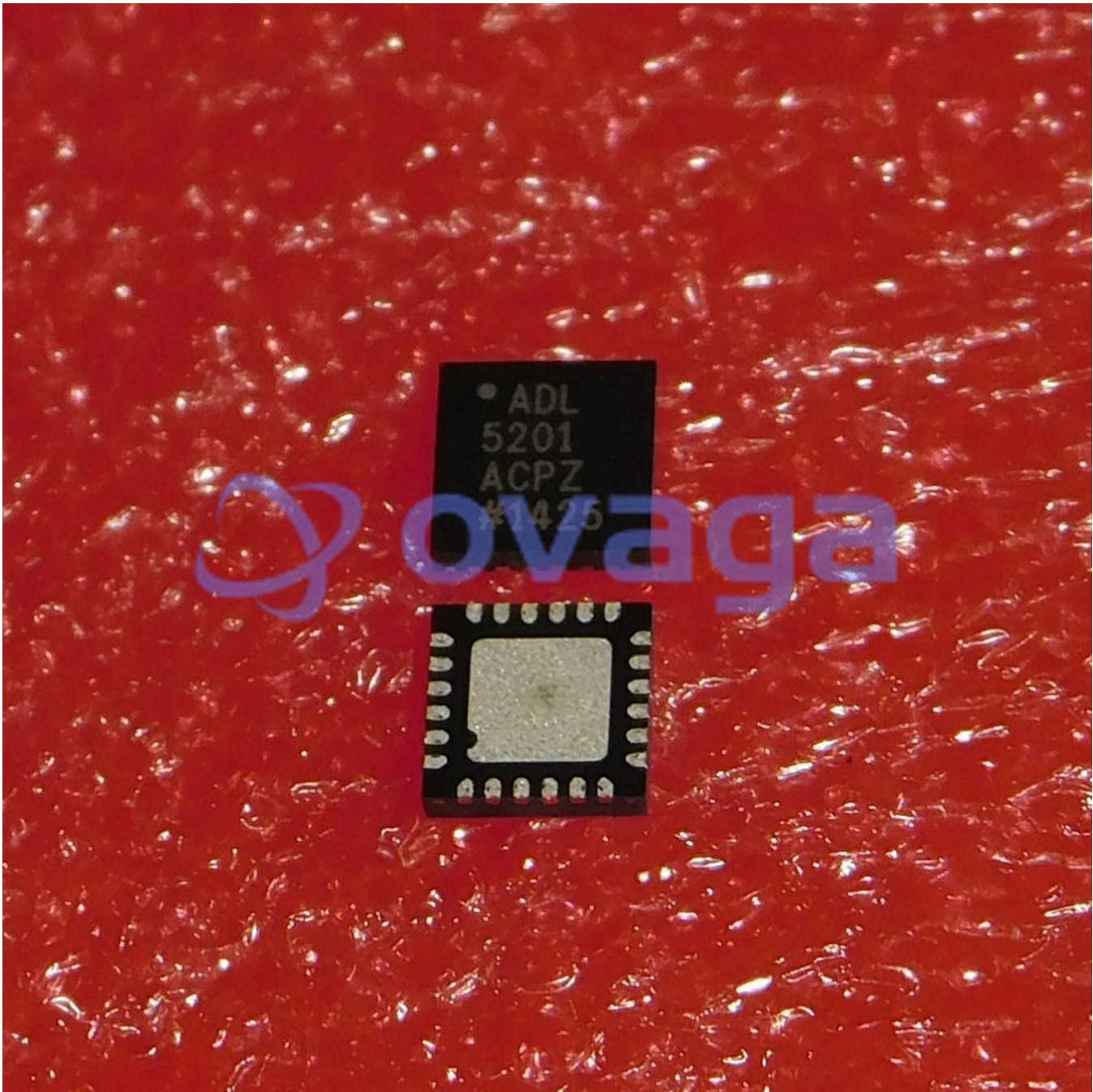
Application

Differential ADC drivers

High IF sampling receivers

High output power IF amplification

Instrumentation



Related Products



[ADL5330ACPZ](#)

Analog Devices, Inc
LFCSP24



[ADL5240ACPZ-R7](#)

Analog Devices, Inc
LFCSP-32



[AD630SD](#)

Analog Devices, Inc
20 ld Side-BrazedCerDIP



[ADRF5040BCPZ](#)

Analog Devices, Inc
HIGH ISOLATION, SP4T, 9KHZ - 12G



[AD607ARSZ-REEL](#)

Analog Devices, Inc
SSOP-20



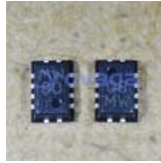
[AD831AP](#)

Analog Devices, Inc
20 ld PLCC



[ADG901BRM](#)

Analog Devices, Inc
MSOP-8



[ADL5350ACPZ](#)

Analog Devices, Inc
LFCSP-8