

EPM7064STC100-7

Data Sheet

CPLD MAX 7000S Family 1.25K Gates 64 Macro Cells 125MHz 5V 100Pin TQFP

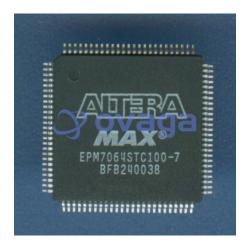
Manufacturers <u>Altera Corporation (Intel)</u>

Package/Case TQFP-100

Product Type Programmable Logic ICs

RoHS

Lifecycle



Images are for reference only

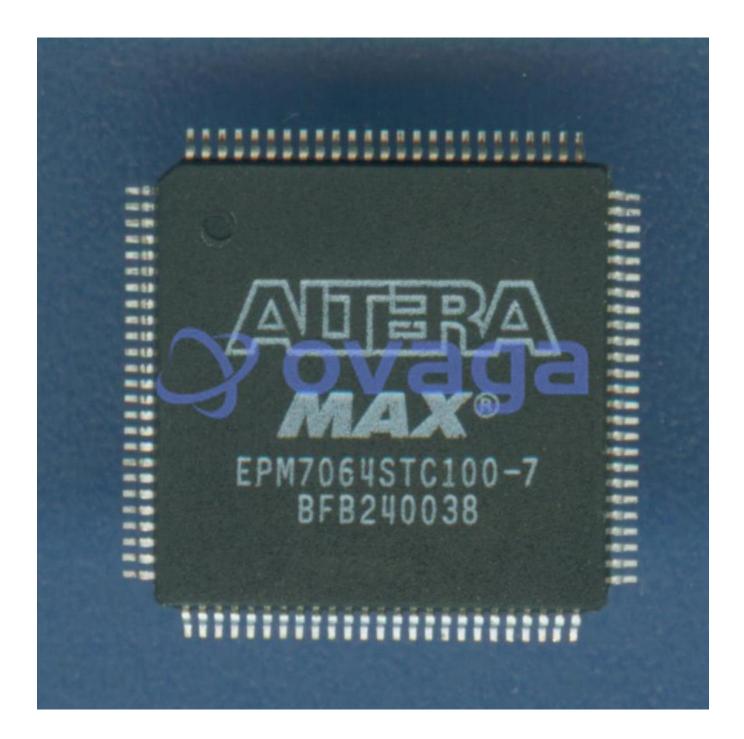
Please submit RFQ for EPM7064STC100-7 or Email to us: sales@ovaga.com We will contact you in 12 hours.

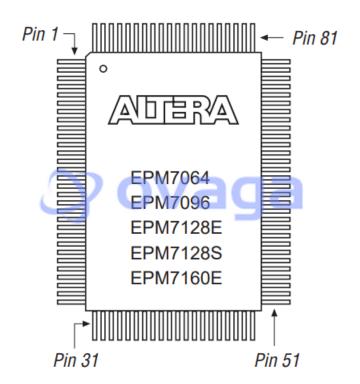
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General Description

EPM7064STC100-7 is a specific type of programmable logic device (PLD) produced by Intel (now Altera) and is part of their MAX 7000 series. It is commonly referred to as an FPGA (field-programmable gate array) and offers a range of digital logic functions that can be programmed by the user.

Features	Application
64 macrocells	Digital signal processing
5V operating voltage	Embedded systems
100-pin TQFP package	Industrial automation
7ns maximum propagation delay	Telecommunications
JTAG boundary scan	Military and aerospace





Related Products



EP4CE55F29C8N

Altera Corporation (Intel) FBGA-780



EPM1270T144A5N

Altera Corporation (Intel) TQFP-144



EP2C35F672C8N

Altera Corporation (Intel) FBGA-672



EP2C35F484C7N

Altera Corporation (Intel) FBGA-484



EPM240M100C5N

Altera Corporation (Intel) BGA-100



EPM570F256C5N

Altera Corporation (Intel) FBGA-256



EPM7128AETC100-10

Altera Corporation (Intel) TQFP-100



EP2C35F484I8N

Altera Corporation (Intel)

FBGA-484