



Data Sheet

Voltage Reference Series - Fixed, 10V reference, 5ppm/°C, DIP-8

Manufacturers	Analog Devices, Inc	6
Package/Case	CDIP-8	
Product Type	Power Management ICs	
RoHS		
Lifecycle		Images are for reference only

Please submit RFQ for AD587UQ or Email to us: sales@ovaga.com We will contact you in 12 hours.

<u>RFO</u>

General Description

The AD587 represents a major advance in state-of-the-art monolithic voltage references. Using a proprietary ion-implanted buried Zener diode and laser wafer trimming of high stability thin-film resistors, the AD587 provides outstanding performance at low cost.

The AD587 offers much higher performance than most other 10 V references. Because the AD587 uses an industry-standard pinout, many systems can be upgraded instantly with the AD587.

The buried Zener approach to reference design provides lower noise and drift than band gap voltage references. The AD587 offers a noisereduction pin that can be used to further reduce the noise level generated by the buried Zener.

The AD587 is recommended for use as a reference for 8-bit, 10-bit, 12-bit, 14-bit, or 16-bit DACs that require an external precision reference. The device is also ideal for successive approximation or integrating ADCs with up to 14 bits of accuracy. In general, it offers better performance than standard on-chip references.

The AD587J and AD587K are specified for operation from 0°C to 70°C, and the AD587U is specified for operation from -55°C to +125°C. The AD587JQ and AD587UQ models are available in 8-lead CERDIP. Other models are available in an 8-lead SOIC package for surface-mount applications, or in an 8-lead PDIP.

Product Highlights

Laser trimming of both initial accuracy and temperature coefficients. This laser trimming results in very low errors over temperature without the use of external components. The AD587U guarantees ± 14 mV maximum total error between -55° C and $+125^{\circ}$ C.

Optional fine trim connection. This connection is designed for applications requiring higher precision.

Instant upgrade of any system using an industry-standard pinout 10 V reference.

Very low output noise. AD587 output noise is typically 4 μ V p-p. A noise-reduction pin is provided for additional noise filtering using an external capacitor.

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Features

Laser trimmed to high accuracy 10.000 V ± 5 mV (U grade) Trimmed temperature coefficient5 ppm/°C maximum (U grade) Noise-reduction capability Low quiescent current: 4 mA maximum Output trim capability

MIL-STD-883-compliant versions available

Related Products



ADP3336ARMZ-REEL7 Analog Devices, Inc MSOP-8



ADP3367ARZ Analog Devices, Inc SOIC-8



ADP3330ARTZ3.3-RL7 Analog Devices, Inc SOT-23-6



ADR421ARZ Analog Devices, Inc SOP-8









ADR3412ARJZ-R7

Analog Devices, Inc SOT-23-6

<u>AD737JRZ</u>

Analog Devices, Inc SOP-8

<u>AD636JH</u>

Analog Devices, Inc TO-100-10

ADR434BRZ

Analog Devices, Inc SOIC-8

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