EPM7256AETC100-7N

Data Sheet

CPLD MAX 7000A Family 5K Gates 256 Macro Cells 126.6MHz CMOS Technology 3.3V 100Pin TQFP

Images are for reference only

Manufacturers	Altera Corporation (Intel)
Package/Case	TQFP-100
Product Type	Programmable Logic ICs
RoHS	Rohs
Lifecycle	

Please submit RFQ for EPM7256AETC100-7N or Email to us: sales@ovaga.com We will contact you in 12 hours. RFQ

General Description

EPM7256AETC100-7N is a type of programmable logic device (PLD) from Intel (formerly Altera). It is a member of the MAX 7000 series of PLDs and features a density of 256 macrocells. The "AETC100" in the part number refers to the package type (a 100-pin TQFP), while the "7N" refers to the speed grade of the device (-7 indicates a maximum operating frequency of 7 MHz).

Features

256 macrocells

5,440 usable gates

56 user I/O pins

5 dedicated inputs

3 global clock pins

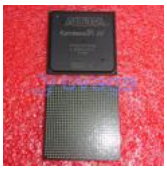
7 ns maximum propagation delay

3.3V operating voltage

ISP (In-System Programmability) support


ALTERA
MAX®
EPM7256AETC100-7N
S CBB770701A
VBB7738140
301SA7CAP

Related Products



[EP4CE55F29C8N](#)

Altera Corporation (Intel)

FBGA-780

[EPM240M100C5N](#)

Altera Corporation (Intel)

BGA-100

[EPM1270T144A5N](#)

Altera Corporation (Intel)

TQFP-144

[EPM570F256C5N](#)

Altera Corporation (Intel)

FBGA-256

[EP2C35F672C8N](#)

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FBGA-672

[EPM7128AETC100-10](#)

Altera Corporation (Intel)

TQFP-100

[EP2C35F484C7N](#)

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FBGA-484

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Altera Corporation (Intel)

FBGA-484