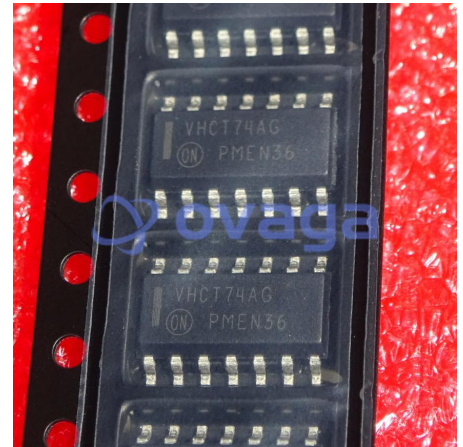


Dual D-Type Flip-Flop with Set and Reset, Flip Flops 5V CMOS Dual D-Type w/Set Reset

Manufacturers	ON Semiconductor, LLC
Package/Case	SOIC-14
Product Type	Logic ICs
RoHS	Rohs
Lifecycle	



Images are for reference only

Please submit RFQ for MC74VHCT74ADR2G or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

[RFQ](#)

General Description

The MC74VHCT74A is an advanced high speed CMOS D-type flip-flop fabricated with silicon gate CMOS technology. It achieves high speed operation similar to equivalent Bipolar Schottky TTL while maintaining CMOS low power dissipation. The signal level applied to the D input is transferred to Q output during the positive going transition of the Clock pulse. Reset (RD) and Set (SD) are independent of the Clock (CP) and are accomplished by setting the appropriate input Low. The internal circuit is composed of three stages, including a buffer output which provides high noise immunity and stable output. The inputs tolerate voltages up to 7V, allowing the interface of 5V systems to 3V systems. The VHCT inputs are compatible with TTL levels. This device can be used as a level converter for interfacing 3.3V to 5.0V, because it has full 5V CMOS level output swings. The VHCT74A input structures provide protection when voltages between 0V and 5.5V are applied, regardless of the supply voltage. The output structures also provide protection when etc.

Features

High Speed: = 5V

Low Power Dissipation: = 25°C

Power Down Protection Provided on Inputs

Balanced Propagation Delays

Designed for 4.5V to 5.5V Operating Range

Low Noise:>

Pin and Function Compatible with Other Standard Logic Families

Latchup Performance Exceeds 300mA

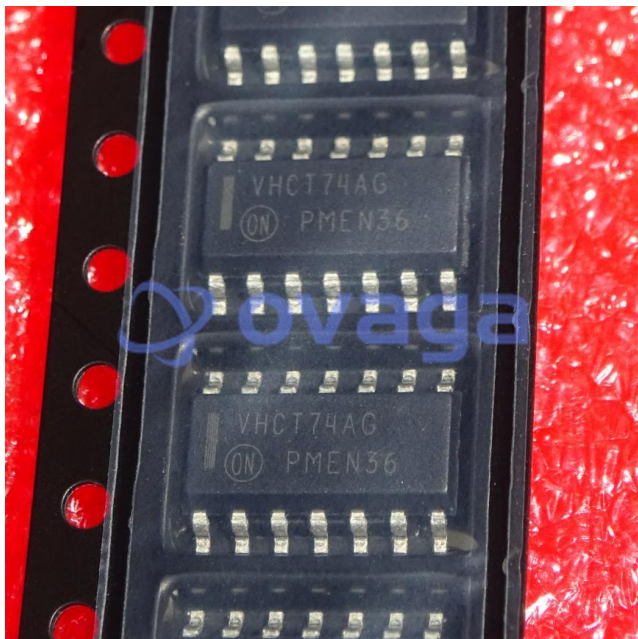
ESD Performance: HBM > 2000V; Machine Model > 200V

Chip Complexity: 128 FETs or 32 Equivalent Gates

Pb-Free Packages are Available

Application

ONSEMI



Related Products



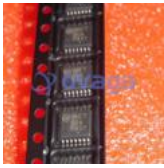
[MC14094BDR2G](#)

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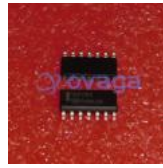
[MC14013BDR2G](#)

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[MC74VHCT50ADTR2G](#)

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TSSOP-14



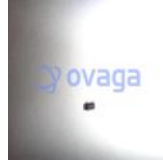
[MC14011BDG](#)

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[MC74VHC1G32DFT1G](#)

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[MC74VHC1G08DFT1G](#)

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[MC74LCX16245DTG](#)

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TSSOP-48



[MC100EP52MNR4G](#)

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QFN-24