

Flip-Flop, with Set and Reset, Complementary Output, Positive Edge, 74HC74, D, 14 ns, 76 MHz, 5.2 mA

Manufacturers	NXP Semiconductor
Package/Case	SOIC-14
Product Type	Logic ICs
RoHS	
Lifecycle	



Images are for reference only

Please submit RFQ for 74HC74D or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

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General Description

74HC74D is a type of integrated circuit (IC) that belongs to the 74HC series of digital logic devices. It is a dual D-type flip-flop IC with a 14-pin SOIC (Small Outline Integrated Circuit) package. The "D" in the name stands for "data" as it is designed to store and manipulate binary data.

Features

Dual D-type flip-flop: It has two independent D-type flip-flops, each with its own clock input, data input, set input, and reset input.

High-speed operation: It operates at high-speed, making it suitable for applications that require rapid data processing.

Wide voltage range: It can operate within a wide voltage range, typically from 2 to 6 volts, which makes it compatible with a variety of digital systems.

Edge-triggered clock inputs: The clock inputs are edge-triggered, which means that the flip-flops respond to changes in the clock signal at the rising or falling edge of the clock pulse.

Asynchronous set and reset inputs: It has asynchronous set (preset) and reset (clear) inputs, which allow for immediate setting or resetting of the flip-flop regardless of the clock input.

Application

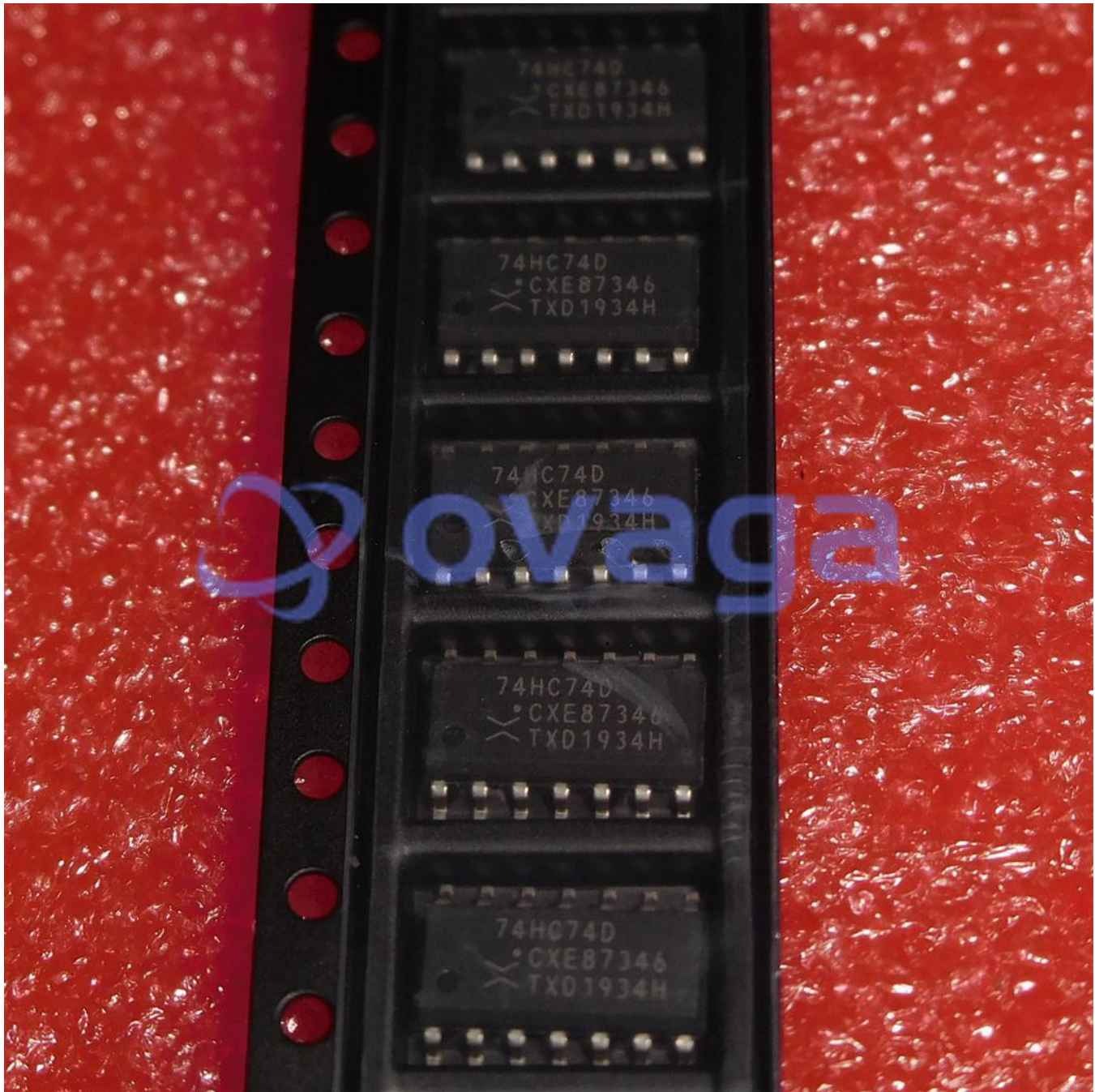
Digital logic circuits: 74HC74D can be used in digital systems for tasks such as data storage, data manipulation, and data synchronization.

Sequential logic circuits: It can be used in sequential logic circuits for tasks such as data latching, data buffering, and data synchronization.

Clock synchronization: It can be used in clock synchronization circuits to align different clock signals in a digital system.

Register and counter circuits: It can be used in register and counter circuits for storing and manipulating data in various applications.



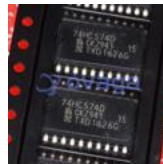


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