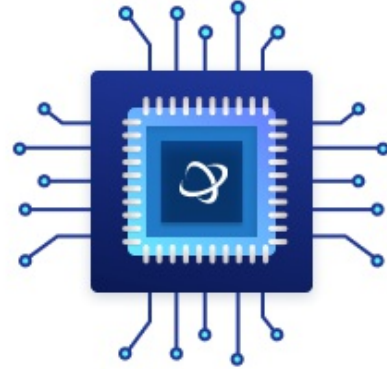


2:4 3.3V PCIe Gen1–5 Clock Mux

Manufacturers	Renesas Technology Corp
Package/Case	
Product Type	Clock & Timer ICs
RoHS	
Lifecycle	



Images are for reference only

Please submit RFQ for 9DML0451AKILF or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

General Description

The 9DML04 devices are 3.3V members of IDT's Full-Featured PCIe family. The 9DML04 supports PCIe Gen1–5 Common Clocked (CC), Separate Reference no Spread (SRnS), and Separate Reference Independent Spread (SRIS) architectures. The part provides a choice of asynchronous and glitch-free switching modes, and offers a choice of integrated output terminations providing direct connection to 85Ω or 100Ω transmission lines. The 9DML04P1 can be factory programmed with a user-defined power up default SMBus configuration.

For information regarding evaluation boards and material, please contact your local IDT sales representative.

Features

PCIe Gen1–5 CC compliant

PCIe Gen1–5 SRIS compliant

Supports PCIe SRnS clocking

Direct connection to 100Ω (xx42) or 85Ω (xx52) transmission lines; saves 16 resistors compared to standard PCIe devices

Spread Spectrum tolerant; allows reduction of EMI

Selectable asynchronous or glitch-free switching; allows the mux to be selected at power up even if both inputs are not running, then transition to glitch-free switching mode

Easy AC-coupling to other logic families, see IDT application note AN-891.

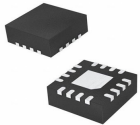
Space saving 4 × 4mm 24-VFQFPN; minimal board space

Related Products



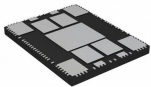
[9DB106BFILE](#)

Renesas Technology Corp
28-SSOP (0.209, 5.30mm Width)



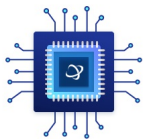
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Renesas Technology Corp
24-VFQFN



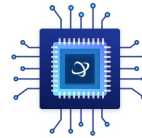
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Renesas Technology Corp
VFQFPN-40



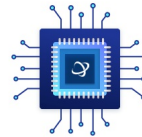
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Renesas Technology Corp



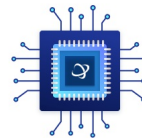
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[9DBL0255NLG18](#)

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