

CPLD - Complex Programmable Logic Devices CPLD - MAX 3000A 64 Macro 66 IOs

Manufacturers	Altera Corporation (Intel)
Package/Case	TQFP-100
Product Type	Programmable Logic ICs
RoHS	
Lifecycle	



Images are for reference only

Please submit RFQ for EPM3064ATC100-10 or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

[RFQ](#)

General Description

EPM3064ATC100-10 is a specific type of Programmable Logic Device (PLD) that belongs to the MAX 3000A family of PLDs developed by Intel Corporation (nowadays Altera, a subsidiary of Intel). The part number EPM3064ATC100-10 specifies the specific device with 64 macrocells, a 100-pin TQFP package, and a maximum operating frequency of 10 MHz.

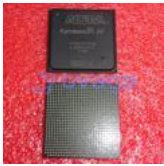
Features

- 64 macrocells
- 36 input pins
- 100-pin TQFP package
- Maximum operating frequency of 10 MHz
- Low power consumption
- Easy to program
- In-system programmable (ISP) via JTAG interface
- Reusable and non-volatile

Application

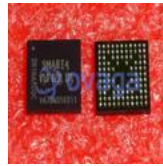
- Digital signal processing
- Communication systems
- Industrial control
- Test and measurement equipment
- Consumer electronics

Related Products



[EP4CE55F29C8N](#)

Altera Corporation (Intel)
FBGA-780



[EPM240M100C5N](#)

Altera Corporation (Intel)
BGA-100



[EPM1270T144A5N](#)

Altera Corporation (Intel)
TQFP-144



[EPM570F256C5N](#)

Altera Corporation (Intel)
FBGA-256



[EP2C35F672C8N](#)

Altera Corporation (Intel)
FBGA-672



[EPM7128AETC100-10](#)

Altera Corporation (Intel)
TQFP-100



[EP2C35F484C7N](#)

Altera Corporation (Intel)
FBGA-484



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FBGA-484