

PIC/DSPIC Microcontroller, PIC24 Family PIC24FV KA Series Microcontrollers, PIC24, 16bit, 32 MHz

Manufacturers	Microchip Technology, Inc
Package/Case	SOIC-28
Product Type	Embedded Processors & Controllers
RoHS	Rohs
Lifecycle	



Images are for reference only

Please submit RFQ for PIC24F16KA102-I/SO or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

[RFQ](#)

General Description

16-bit Microcontroller featuring XLP for Xtreme Low Power consumption. Designed for power constrained and battery powered applications. Features unique peripherals like deep sleep mode DSBOR, DSWDT and RTCC for industry leading low power performance.

Features

Typical nanoWatt XLP specifications include:

20nA Deep Sleep mode

25nA Sleep mode (RAM retention)

500nA Real Time Clock & Calendar operation in Sleep modes

400nA Watch Dog Timer operation in Sleep modes

512 Bytes of Data EEPROM

Other Low Power Specifications include:

5 μ S wake-up from Sleep

50nA I/O port leakage

195 μ A at 1MHz Run mode

Power Modes: Run, Doze, Idle, Sleep, Deep Sleep

System Supervisors: Low Power BOR, WDT, INT0 and RTCC

Internal oscillator support - 31 kHz to 8 MHz, up to 32 MHz with 4X PLL

Fail-Safe Clock Monitor – allows safe shutdown if clock fails

CPU:

Up to 16 MIPS performance

Single Cycle Instruction Execution

16 x 16 Hardware Multiply, & 32-bit x 16-bit Hardware Divider

C Compiler Optimized Instruction Set System

Peripherals:

10-bit Differential ADC, 9 channels, 500k samples per second, 16-deep result buffer

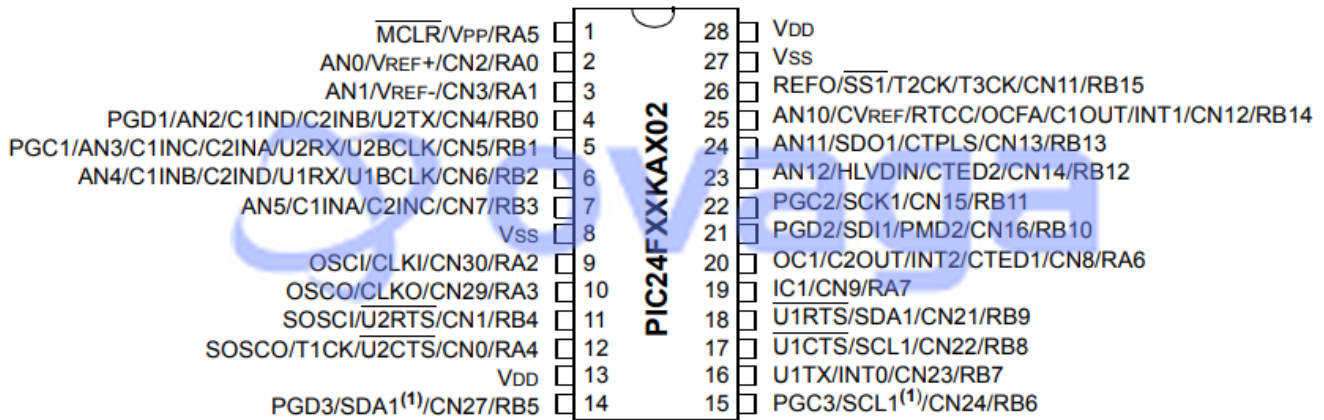
Charge Time Measurement Unit (CTMU) enabling 9 channels of Capacitive Touch

Two Analog rail-to-rail comparators Peripherals

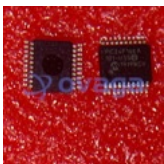
2 UART Modules with LIN and IrDA support, 4 Deep FIFO

SPI Modules with 8 Deep FIFO

Hardware RTCC, Real-Time Clock Calendar with Alarms



Related Products



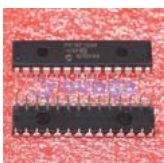
[PIC24F16KA101-I/SS](#)

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SSOP-20



[PIC16F1936-I/SS](#)

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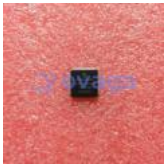
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