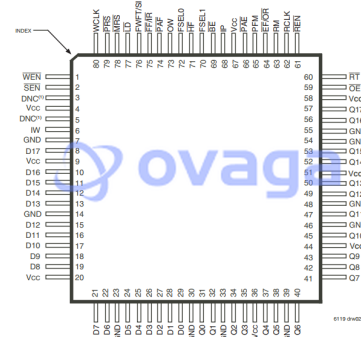


IC FIFO SUPERSYNCHII 10NS 80TQFP; HT SUSA CODE:8542320070

Manufacturers	Renesas Technology Corp
Package/Case	TQFP-80
Product Type	Memory
RoHS	
Lifecycle	



Images are for reference only

Please submit RFQ for 72V2113L10PFI or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

[RFQ](#)

General Description

The 72V2113 512K x 9/256K x 18 SuperSync II FIFO memory has flexible x9/x18 Bus-Matching on both read and write ports. The variable clock cycle counting delay associated with the latency period found on previous SuperSync devices has been eliminated. SuperSync II FIFOs are appropriate for network, video, telecommunications, data communications and other applications that need to buffer large amounts of data and match buses of unequal sizes.

Features

Functionally compatible with the 72V255/65/75/85 SuperSync FIFOs

Up to 166 MHz Operation of the Clocks

User selectable Asynchronous read and/or write ports (BGA Only)

User selectable input and output port bus-sizing

Pin to Pin compatible to the SuperSync II 72V2x3 devices

5V tolerant inputs

Auto power down minimizes standby power consumption

Master Reset clears entire FIFO

Partial Reset clears data, but retains programmable settings

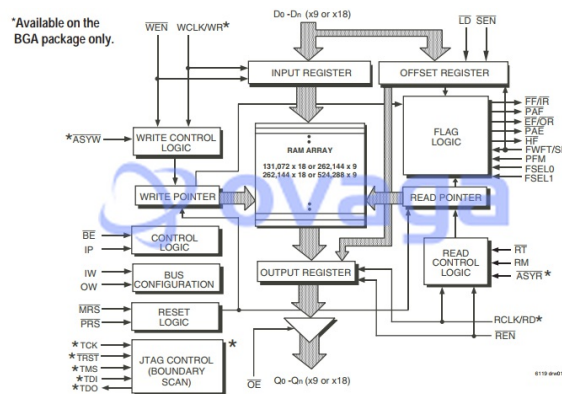
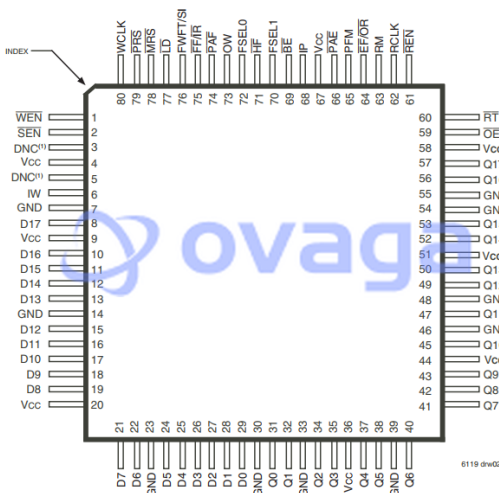
Easily expandable in depth and width

JTAG port, provided for Boundary Scan function (BGA Only)

Independent Read and Write Clocks

Available in a 80-pin TQFP or a 100-pin BGA packages

Industrial temperature range (-40C to +85C) is available



Related Products



[7205L15J](#)

Renesas Technology Corp
PLC32



[7204L25JL](#)

Renesas Technology Corp
32-LCC (J-Lead)



[72V36110L10PF](#)

Renesas Technology Corp
TQFP-100



[72V275L15PF](#)

Renesas Technology Corp
TQFP-64



[7201LA50TDB](#)

Renesas Technology Corp
CDIP28



[72V261LA10TEG](#)

Renesas Technology Corp
64-LQFP



[7204L12J](#)

Renesas Technology Corp
32-LCC (J-Lead)



[7207L20DB](#)

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CDIP-28