

Latches 8-Bit Serial Input Protected Latched Driver (Lead Free)

Manufacturers	Microchip Technology, Inc
Package/Case	SOIC-18
Product Type	Power Management ICs
RoHS	Rohs
Lifecycle	



Images are for reference only

Please submit RFQ for MIC58P42YWM or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

[RFQ](#)

General Description

The MIC58P42 serial-input latched driver is a high-voltage (80V), high-current (500mA) integrated circuit comprised of eight CMOS data latches, a bipolar Darlington transistor driver for each latch, and CMOS control circuitry for the common STROBE, CLOCK, SERIAL DATA INPUT, and OUTPUT ENABLE functions. Similar to the MIC5842, additional protection circuitry supplied on this device includes thermal shutdown, under voltage lockout (UVLO), and over-current shutdown. The bipolar/CMOS combination provides an extremely low-power latch with maximum interface flexibility. The MIC58P42 has open-collector outputs capable of sinking 500 mA and integral diodes for inductive load transient suppression with a minimum output breakdown voltage rating of 80V (50V sustaining). The drivers can be operated with a split supply, where the negative supply is down to -20V and may be paralleled for higher load current capability. With a 5V logic supply, the MIC58P42 will typically operate at better than 5MHz. With a 12V logic supply, significantly higher speeds are obtained. The CMOS inputs are compatible with standard CMOS, PMOS, and NMOS circuits. TTL circuits may require pull-up resistors. By using the serial data output, drivers may be cascaded for interface applications requiring additional drive lines. Each of these eight outputs has an independent over current shutdown of 500 mA. Upon over-current detection, the affected channel will turn OFF until VDD is cycled or the ENABLE/RESET pin is pulsed high. Current pulses less than 2µs will not activate current shutdown. Temperatures above 165°C will shut down the device. The UVLO circuit prevents operation at low VDD; hysteresis of 0.5V is provided. See the MIC59P60 for a similar device that additionally provides an error flag output.

Features

3.3 MHz Minimum Data-Input Rate

CMOS, PMOS, NMOS, and TTL Compatible

Internal Pull-Up/Pull-Down Resistors

Low Power CMOS Logic and Latches

High Voltage (80V) Current-Sink Outputs

Output Transient-Protection Diodes

Single or Split Supply Operation

Thermal Shutdown

Under-Voltage Lockout

Per-Output Over-Current Shutdown (500mA typical)

Related Products



[MIC94325YMT-TR](#)

Microchip Technology, Inc
UDFN-6



[MIC4684YM](#)

Microchip Technology, Inc
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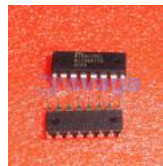
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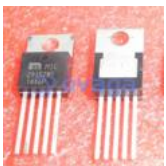
[MIC5841YWM-TR](#)

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[MIC5891YN](#)

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