



Data Sheet

Pb-Free SWITCH 4X SPDT +/-5V 18 OHM 20SSOP IND HT SUSA CODE:8542390000

Manufacturers Renesas Technology Corp

Package/Case SSOP-20

Product Type Interface ICs

RoHS Rohs

Lifecycle



Images are for reference only

Please submit RFQ for ISL43240IAZ or Email to us: sales@ovaga.com We will contact you in 12 hours.

RFO

General Description

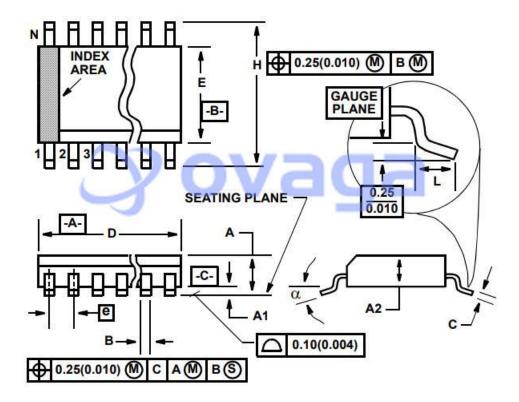
The Intersil ISL43240 device is a CMOS, precision, quad SPDT analog switch designed to operate from a single $\pm 2V$ to $\pm 12V$ supply or from a $\pm 2V$ to $\pm 6V$ supply. Targeted applications include battery powered equipment that benefit from the devices' low power consumption (5 μ W), low leakage currents (5nA max), and fast switching speeds = 40ns). A 5 Ω maximum rON flatness ensures signal fidelity, while channel-to-channel mismatch is guaranteed to be less than 2 Ω . The ISL43240 is a quad single-pole/double-throw (SPDT) device and can be used as a quad SPDT, a quad 2:1 multiplexer, a single 4:1 multiplexer or a dual 2-channel differential multiplexer. Table 1 summarizes the performance of this family.

Features

Fully specified for 10% tolerances at = $12V$, $5V$ and $3.3V$
Four separately controlled SPDT switches
ON-resistance (r_{ON}): 18Ω
r_{ON} matching between channels: ${<}1\Omega$
Low charge injection: 5pC (Max)
Low power consumption (P_D): $<5\mu W$
Low off leakage current (max at +85°C): 2.5nA
Fast switching action
t _{ON} : 52ns
t _{OFF} : 40ns
Guaranteed break-before-make
Minimum 2000V ESD protection per Method 3015.7
TTL, CMOS compatible
Pb-free (RoHS compliant)



Shrink Small Outline Plastic Packages (SSOP)



Related Products



ISL83491IBZ

Renesas Technology Corp SOIC-14



ISL99227FRZ-T

Renesas Technology Corp 32-PowerWFQFN



ISL83072EIBZA

Renesas Technology Corp SOIC-8



IS82C55AZ

Renesas Technology Corp PLCC-44



ISL3170EIUZ

Renesas Technology Corp MSOP-10



ISL83078EIBZA

Renesas Technology Corp SOIC-8



ISL81487EIBZ

Renesas Technology Corp SOP-8



ISL83387EIVZ

Renesas Technology Corp TSSOP-24