

Digital to Analogue Converter, 12 bit, 125 MSPS, Parallel, 2.7V to 5.5V, SOIC, 28 Pins

Manufacturers	Analog Devices, Inc
Package/Case	SOIC-28
Product Type	Data Conversion ICs
RoHS	Rohs
Lifecycle	Images are for reference only



Please submit RFQ for AD9762ARZ or [Email to us: sales@ovaga.com](#) We will contact you in 12 hours.

[RFQ](#)

General Description

The AD9762 is the 12-bit resolution member of the TxDAC series of high performance, low power CMOS digital-to-analog converters (DACs). The TxDAC family which consists of pin compatible 8-, 10-, 12-, and 14-bit DACs is specifically optimized for the transmit signal path of communication systems. All of the devices share the same interface options, small outline package and pinout, thus providing an upward or downward component selection path based on performance, resolution and cost. The AD9762 offers exceptional ac and dc performance while supporting update rates up to 125 MSPS.

The AD9762's flexible single-supply operating range of 2.7 V to 5.5 V and low power dissipation are well suited for portable and low power applications. Its power dissipation can be further reduced to a mere 45 mW without a significant degradation in performance by lowering the full-scale current output. Also, a powerdown mode reduces the standby power dissipation to approximately 25 mW.

The AD9762 is manufactured on an advanced CMOS process. A segmented current source architecture is combined with a proprietary switching technique to reduce spurious components and enhance dynamic performance. Edge-triggered input latches and a 1.2 V temperature compensated bandgap reference have been integrated to provide a complete monolithic DAC solution. Flexible supply options support +3 V and +5 V CMOS logic families.

The AD9762 is a current-output DAC with a nominal full-scale output current of 20 mA and > 100 k(ohm) output impedance.

Differential current outputs are provided to support single-ended or differential applications. Matching between the two current outputs ensures enhanced dynamic performance in a differential output configuration. The current outputs may tied directly to an output resistor to provide two complementary, single-ended voltage outputs or fed directly into a transformer. The output voltage compliance range is 1.25 V.

The on-chip reference and control amplifier are configured for maximum accuracy and flexibility. The AD9762 can be driven by the on-chip reference or by a variety of external reference voltages. The internal control amplifier which provides a wide (>10:1) adjustment span allows the AD9762 full-scale current to be adjusted over a 2 mA to 20 mA range while maintaining excellent dynamic performance. Thus, the AD9762 may operate at reduced power levels or be adjusted over a 20 dB range to provide additional gain ranging capabilities.

The AD9762 is available in a 28-pin SOIC package. It is specified for operation over the industrial temperature range.

Features

Member of Pin-Compatible TxDAC Product Family

125 MSPS Update Rate

12-Bit Resolution

Excellent Spurious Free Dynamic Range Performance

SFDR to Nyquist @ 5 MHz Output: 70 dBc

Differential Current Outputs: 2 mA to 20 mA

Power Dissipation: 175 mW @ 5 V to 45 mW @ 3 V

Power-Down Mode: 25 mW @ 5 V

On-Chip 1.20 V Reference

Single +5 V or +3 V Supply Operation

Package: 28-Lead SOIC and TSSOP

Edge-Triggered Latches

Related Products



[ADAS3022BCPZ](#)

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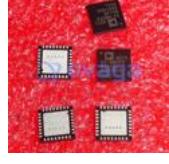
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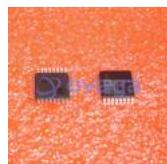
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