

LDO Regulator Pos 1.22V to 19V 0.3A 8-Pin LFCSP EP T/R

Manufacturers	Analog Devices, Inc
Package/Case	LFCSP-8
Product Type	Power Management ICs
RoHS	Rohs
Lifecycle	



Images are for reference only

Please submit RFQ for ADP7102ACPZ-R7 or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

[RFQ](#)

General Description

The ADP7102 is available in seven fixed output voltage options and an adjustable version, which allows output voltages that range from 1.22 V to $V_{IN} - V_{DO}$ via an external feedback divider.

The ADP7102 output noise voltage is 15 μ V rms and is independent of the output voltage. A digital power good output allows power system monitors to check the health of the output voltage. A user programmable precision undervoltage lockout function facilitates sequencing of multiple power supplies.

The ADP7102 is available in 8-lead, 3 mm \times 3 mm LFCSP and 8-lead SOIC packages. The LFCSP offers a very compact solution and also provides excellent thermal performance for applications requiring up to 300 mA of output current in a small, low-profile footprint.

Features

Input voltage range: 3.3 V to 20 V

Maximum output current: 300 mA

Low noise: 15 μ V rms for fixed output versions

PSRR performance of 60 dB at 10 kHz>

Reverse current protection

Low dropout voltage: 200 mV at 300 mA load

Initial accuracy: $\pm 0.8\%$

See data sheet for additional features

Application

Regulation to noise sensitive applications: ADC, DAC circuits, precision amplifiers, high frequency oscillators, clocks, and phase-locked loops

Communications and infrastructure

Medical and healthcare

Industrial and instrumentation



Related Products



[ADP3336ARMZ-REEL7](#)

Analog Devices, Inc
MSOP-8



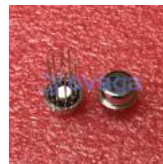
[AD737JRZ](#)

Analog Devices, Inc
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[ADP3367ARZ](#)

Analog Devices, Inc
SOIC-8



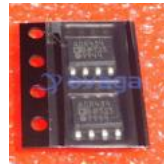
[AD636JH](#)

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TO-100-10



[ADP3330ARTZ3.3-RL7](#)

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SOT-23-6



[ADR434BRZ](#)

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[ADR421ARZ](#)

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[ADR3412ARJZ-R7](#)

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