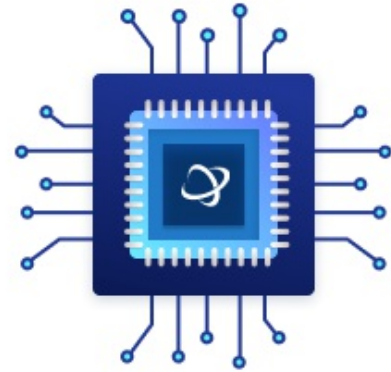


8-Output DB800ZL PCIe Zero-Delay/Fanout Clock Buffer

Manufacturers	Renesas Technology Corp
Package/Case	
Product Type	Clock & Timer ICs
RoHS	
Lifecycle	



Images are for reference only

Please submit RFQ for 9ZXL0851EKILFT or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

RFQ

General Description

The 9ZXL0851E is a second-generation enhanced performance DB800ZL differential buffer. The part is a pin-compatible upgrade to the 9ZXL0851A, while offering a much improved phase jitter performance. A fixed external feedback maintains low drift for critical QPI/UPI applications.

Features

LP-HCSL outputs with 85Ω Zout; eliminate 32 resistors, save 64mm² of area

PCIe Gen 1–5 compliance

8 OE# pins; hardware control of each output

Selectable PLL BW; minimizes jitter peaking in cascaded PLL topologies

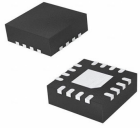
Hardware/SMBus control of PLL bandwidth and bypass; change mode without power cycle

Spread spectrum compatible; tracks spreading input clock for EMI reduction

100MHz and 133.33MHz PLL mode; UPI and legacy QPI support

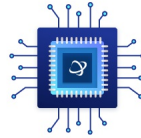
6 × 6 mm 48-VFQFPN package; small board footprint

Related Products



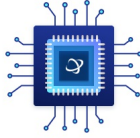
[9ZX21901CKLF](#)

Renesas Technology Corp
72-VFQFN



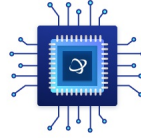
[9ZXL1251EKILFT](#)

Renesas Technology Corp



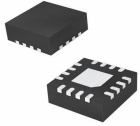
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Renesas Technology Corp



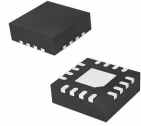
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Renesas Technology Corp



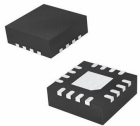
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Renesas Technology Corp
72-VFQFN



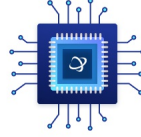
[9ZXL1930BKLF](#)

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[9ZXL1550BKLF](#)

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