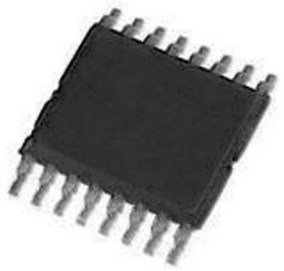
ADN4668ARUZ

Data Sheet

3 V LVDS Quad CMOS Differential Line Receiver; Package: TSSOP 4.4 MM; No of Pins: 16; Temperature Range: Industrial

Manufacturers	Analog Devices, Inc
Package/Case	TSSOP-16
Product Type	Interface ICs
RoHS	Rohs
Lifecycle	



Images are for reference only

Please submit RFQ for ADN4668ARUZ or Email to us: sales@ovaga.com We will contact you in 12 hours. RFQ

General Description

The ADN4668 also offers active-high and active-low enable/disable inputs (EN and overbar: EN) that control all four receivers. They disable the receivers and switch the outputs to a high impedance state. This high impedance state allows the outputs of one or more ADN4668s to be multiplexed together and reduces the quies-cent power consumption to 3 mW typical. The ADN4668 and its companion driver, the ADN4667, offer a new solution to high speed, point-to-point data transmission and a low power alternative to emitter-coupled logic (ECL) or positive emitter-coupled logic (PECL).

Features

400 Mbps (200 MHz) switching rates

Flow-through pin configuration simplifies PCB layout

150 ps channel-to-channel skew (typical)

100 ps differential skew (typical)

2.7 ns maximum propagation delay

3.3 V power supply

High impedance outputs on power-down

Low power design (3 mW quiescent typical)

Interoperable with existing 5 V LVDS drivers

Accepts small swing (310 mV typical) differential input signal levels

Supports open, short, and terminated input fail-safe

Related Products

ADV7181CBSTZ

Analog Devices, Inc

LQFP-64

AD8170AR

Analog Devices, Inc

SOP8

AD724JR

Analog Devices, Inc

SOIC-16

ADV7393BCPZ

Analog Devices, Inc

LFCSP-VQ-40

ADV7391WBCPZ

Analog Devices, Inc

LFSCP-3

ADV7390BCPZ

Analog Devices, Inc

QFN32

ADV7341BSTZ

Analog Devices, Inc

LQFP-64

ADUM4160BRIZ

Analog Devices, Inc

SOIC-16