

DAC 4-CH R-2R 14-bit 28-Pin SOIC W Tube

Manufacturers	Analog Devices, Inc
Package/Case	SOIC-28
Product Type	Data Conversion ICs
RoHS	Rohs
Lifecycle	



Images are for reference only

Please submit RFQ for AD7834ARZ or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

[RFQ](#)

General Description

The AD7834 and AD7835 contain four 14-bit DACs on one monolithic chip. The AD7834 and AD7835 have output voltages in the range ± 8.192 V with a maximum span of 14 V.

The AD7834 is a serial input device. Data is loaded in 16-bit format from the external serial bus, MSB first after two leading 0s, into one via DIN, SCLK, and FSYNC. The AD7834 has five dedicated package address pins, PA0 to PA4, that can be wired to AGND or VCC to permit up to 32 AD7834s to be individually addressed in a multipackage application.

The AD7835 can accept either 14-bit parallel loading or double-byte loading, where right-justified data is loaded in one 8-bit byte and one 6-bit byte. Data is loaded from the external bus into one of the input latches under the control of the WR, CS, BYSHF, and DAC channel address pins, A0 to A2.

With each device, the LDAC signal is used to update all four DAC outputs simultaneously, or individually, on reception of new data. In addition, for each device, the asynchronous CLR input can be used to set all signal outputs, VOUT1 to VOUT4, to the user-defined voltage level on the device sense ground pin, DSG. On power-on, before the power supplies have stabilized, internal circuitry holds the DAC output voltage levels to within ± 2 V of the DSG potential. As the supplies stabilize, the DAC output levels move to the exact DSG potential (assuming CLR is exercised).

The AD7834 is available in a 28-lead 0.3" SOIC package and a 28-lead 0.6" PDIP package, and the AD7835 is available in a 44-lead MQFP package and a 44-lead PLCC package.

Features

Four 14-bit DACs in one package

AD7834—serial loading

AD7835—parallel 8-bit/14-bit loading

Voltage outputs

Power-on reset function

Maximum/minimum output voltage range of ± 8.192 V

Maximum output voltage span of 14 V

Common voltage reference inputs

User-assigned device addressing

Clear function to user-defined voltage

Surface-mount packages

Packages: AD7834—28-lead SOIC and PDIP AD7835—44-lead MQFP and PLCC

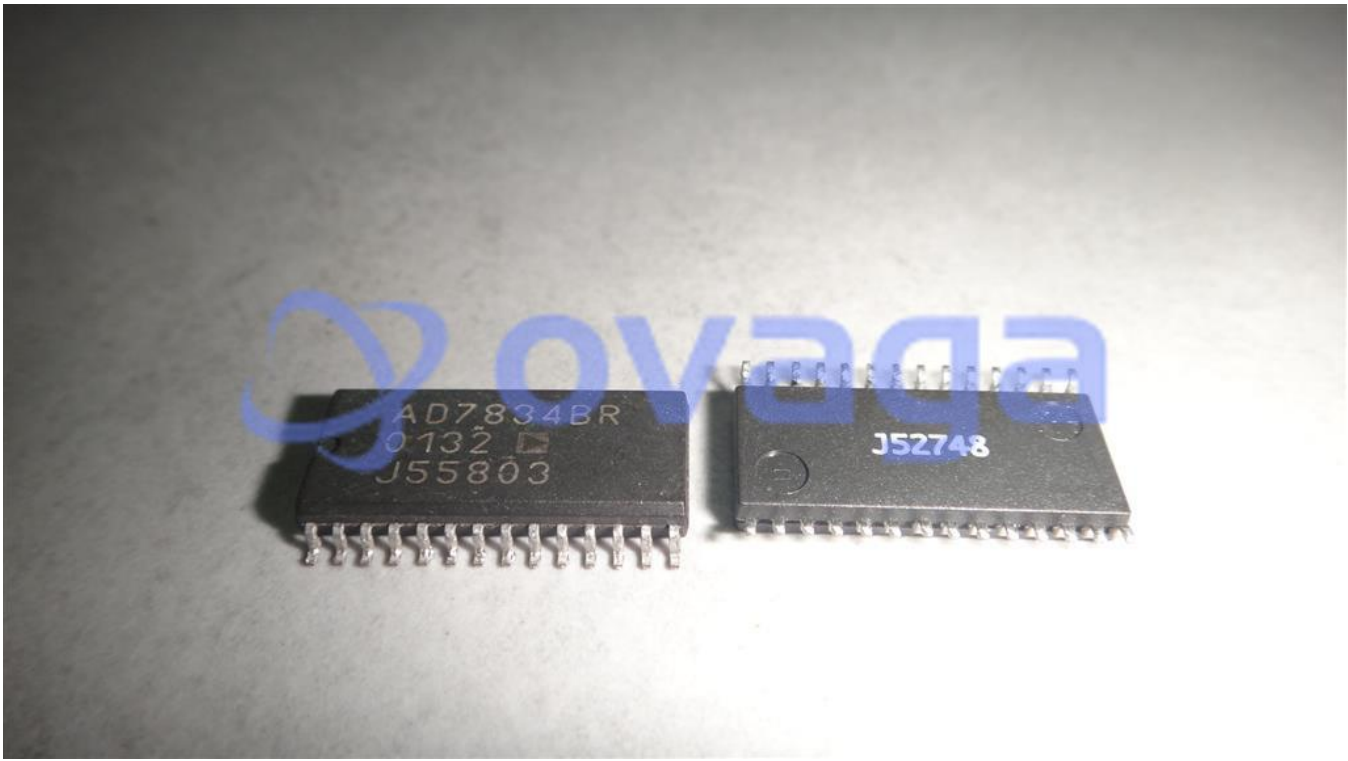
Application

Process control

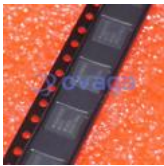
Automatic test equipment

General-purpose instrumentation





Related Products



[ADAS3022BCPZ](#)

Analog Devices, Inc
LFCSP-40



[AD574AJNZ](#)

Analog Devices, Inc
PDIP-28



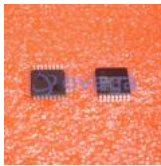
[AD7938BSUZ](#)

Analog Devices, Inc
TQFP-32



[AD7124-8BCPZ-RL7](#)

Analog Devices, Inc
LFCSP-32



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SOIC-16



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