

32Bit MCU 128KB Flash 16KB RAM 50MHz 64Pin USB CAN 3 Comp CTMU RTCC

Manufacturers	Microchip Technology, Inc
Package/Case	QFN-64
Product Type	Embedded Processors & Controllers
RoHS	
Lifecycle	



Images are for reference only

Please submit RFQ for PIC32MX530F128HT-I/MR or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

[RFQ](#)

General Description

Features

Up to 50 MHz/83 DMIPS, MIPS32® M4K® core

USB 2.0-compliant Full-speed OTG controller

Controller Area Network (CAN) 2.0B Compliant with DeviceNet™ addressing support

Three I2S/SPI modules for Codec and serial communications

Peripheral Pin Select (PPS) functionality

Parallel Master Port (PMP) with dual read/write buffers for graphics interfaces

mTouch™ Capacitive touch

Temperature Range - 40°C to 105°C

Microcontroller Features

Operating voltage range of 2.3V to 3.6V

128KB Flash memory (plus an additional 3 KB of Boot Flash)

16KB SRAM memory

MIPS16e® mode for up to 40% smaller code size

Low-power management modes (Sleep and Idle)

Peripheral Features

Peripheral Pin Select (PPS) functionality to allow function remap

Four channels of hardware DMA with automatic data size detection

Two additional DMA channels dedicated to USB

Two additional DMA channels dedicated to CAN

Four UARTs (12.5 Mbps) and two I2C™ modules

Hardware Real-Time Clock and Calendar (RTCC)

Five 16-bit and up to two 32-bit Timers/Counters

Five Capture inputs and Five Compare/PWM outputs

Audio/Graphics/Touch HMI Features

External graphics interface with up to 34 PMP pins

Audio data communication: I2S, LJ, RJ, USB

Audio data control interface: SPI and I2C™

Audio data master clock:

Generation of fractional clock frequencies

Can be synchronized with USB clock

Can be tuned in run-time

Supports miTouch™ capacitive touch sensing

Advanced Analog Features

ADC Module:

10-bit 1 Msps rate with one Sample and Hold (S&H)

28 analog inputs

Can operate during sleep mode

Flexible and independent ADC trigger sources

Comparators:

Three dual-input Comparator modules

Programmable references with 32 voltage points

Debugger Development Support

In-circuit and in-application programming

4-wire MIPS® Enhanced JTAG interface

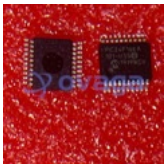
Unlimited program and six complex data breakpoints

IEEE 1149.2-compatible (JTAG) boundary scan



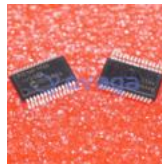


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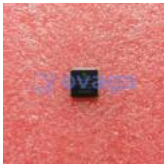
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