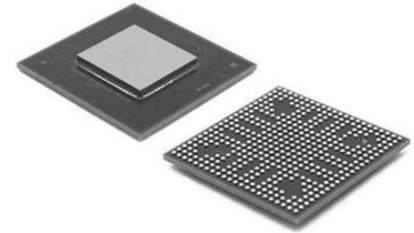


Analogue to Digital Converter, 14 bit, 50 MSPS, Differential, Single Ended, SPI, Single, 1.7 V

Manufacturers	<a href="#">Analog Devices, Inc</a>
Package/Case	LFCSP-48
Product Type	Data Conversion ICs
RoHS	Rohs
Lifecycle	



Images are for reference only

Please submit RFQ for AD9259ABCPZ-50 or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

[RFQ](#)

## General Description

The ADC requires a single 1.8 V power supply and LVPECL-/ CMOS-/LVDS-compatible sample rate clock for full performance operation. No external reference or driver components are required for many applications.

The ADC automatically multiplies the sample rate clock for the appropriate LVDS serial data rate. A data clock output (DCO) for capturing data on the output and a frame clock output (FCO) for signaling a new output byte are provided. Individual-channel power-down is supported and typically consumes less than 2 mW when all channels are disabled.

The ADC contains several features designed to maximize flexibility and minimize system cost, such as programmable clock and data alignment and programmable digital test pattern generation. The available digital test patterns include built-in deterministic and pseudorandom patterns, along with custom user-defined test patterns entered via the serial port interface (SPI).

The AD9259 is available in a RoHS compliant, 48-lead LFCSP. It is specified over the industrial temperature range of  $-40^{\circ}\text{C}$  to  $+85^{\circ}\text{C}$ .

### Product Highlights

**Small Footprint.** Four ADCs are contained in a small, space-saving package.

**Low power** of 98 mW/channel at 50 MSPS.

**Ease of Use.** A data clock output (DCO) operates at frequencies of up to 350 MHz and supports double data rate (DDR) operation.

**User Flexibility.** The SPI control offers a wide range of flexible features to meet specific system requirements.

**Pin-Compatible Family.** This includes the AD9287 (8-bit), AD9219 (10-bit), and AD9228 (12-bit).

## Features

4 ADCs integrated into 1 package

98 mW ADC power per channel at 50>

Excellent>

Serial LVDS (ANSI-644, default)

Low power, reduced signal option (similar to IEEE 1596.3)

Data and frame clock outputs

315 MHz full-power analog bandwidth

2 V p-p input voltage range

1.8 V supply operation

Please refer to the data sheet for more information

## Application

Medical imaging and nondestructive ultrasound

Portable ultrasound and digital beam-forming systems

Quadrature radio receivers

Diversity radio receivers

Tape drives

Optical networking

Test equipment

## Related Products



[ADAS3022BCPZ](#)

Analog Devices, Inc  
LFCSP-40



[AD574AJNZ](#)

Analog Devices, Inc  
PDIP-28



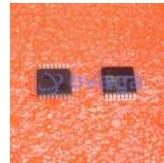
[AD7938BSUZ](#)

Analog Devices, Inc  
TQFP-32



[AD7124-8BCPZ-RL7](#)

Analog Devices, Inc  
LFCSP-32



[AD7266BSUZ](#)

Analog Devices, Inc  
TQFP-32



[AD7401YRWZ](#)

Analog Devices, Inc  
SOIC-16



[AD7192BRUZ-REEL](#)

Analog Devices, Inc  
TSSOP-24



[AD9680BCPZ-500](#)

Analog Devices, Inc  
LFCSP-64