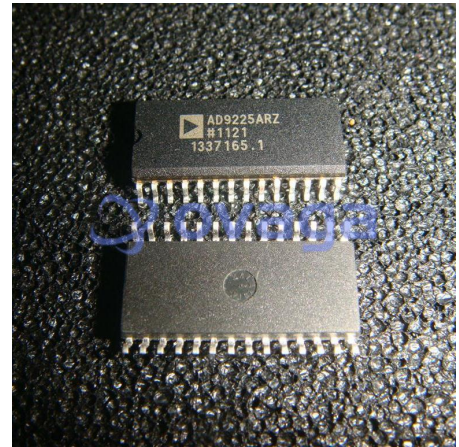


1-Channel Single ADC Pipelined 25Msps 12-bit Parallel 28-Pin SOIC W Tube

Manufacturers	Analog Devices, Inc
Package/Case	SOIC-28
Product Type	Data Conversion ICs
RoHS	Rohs
Lifecycle	



Images are for reference only

Please submit RFQ for AD9225ARZ or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

[RFQ](#)

General Description

The AD9225 is a monolithic, single supply 12-bit, 25MSPS Analog to Digital Converter with an on-chip, high performance sample-and-hold amplifier and voltage reference. The AD9225 uses a multi-stage differential pipelined architecture with output error correction logic to provide 12-bit accuracy at 25MSPS data rates and guarantees no missing codes over the full operating temperature range.

PRODUCT HIGHLIGHTS

The AD9225 is fabricated on a very cost effective CMOS process. High speed precision analog circuits are combined with high density logic circuits.

The AD9225 offers a complete, single-chip sampling, 12-bit, 25 MSPS analog-to-digital conversion function in 28-lead SOIC and SSOP packages.

Low Power—The AD9225 at 280 mW consumes a fraction of the power presently available in monolithic solutions.

On-Board Sample-and-Hold Amplifier (SHA)—The versatile SHA input can be configured for either single-ended or differential inputs.

Out-of-Range (OTR)—The OTR output bit indicates when the input signal is beyond the AD9225's input range.

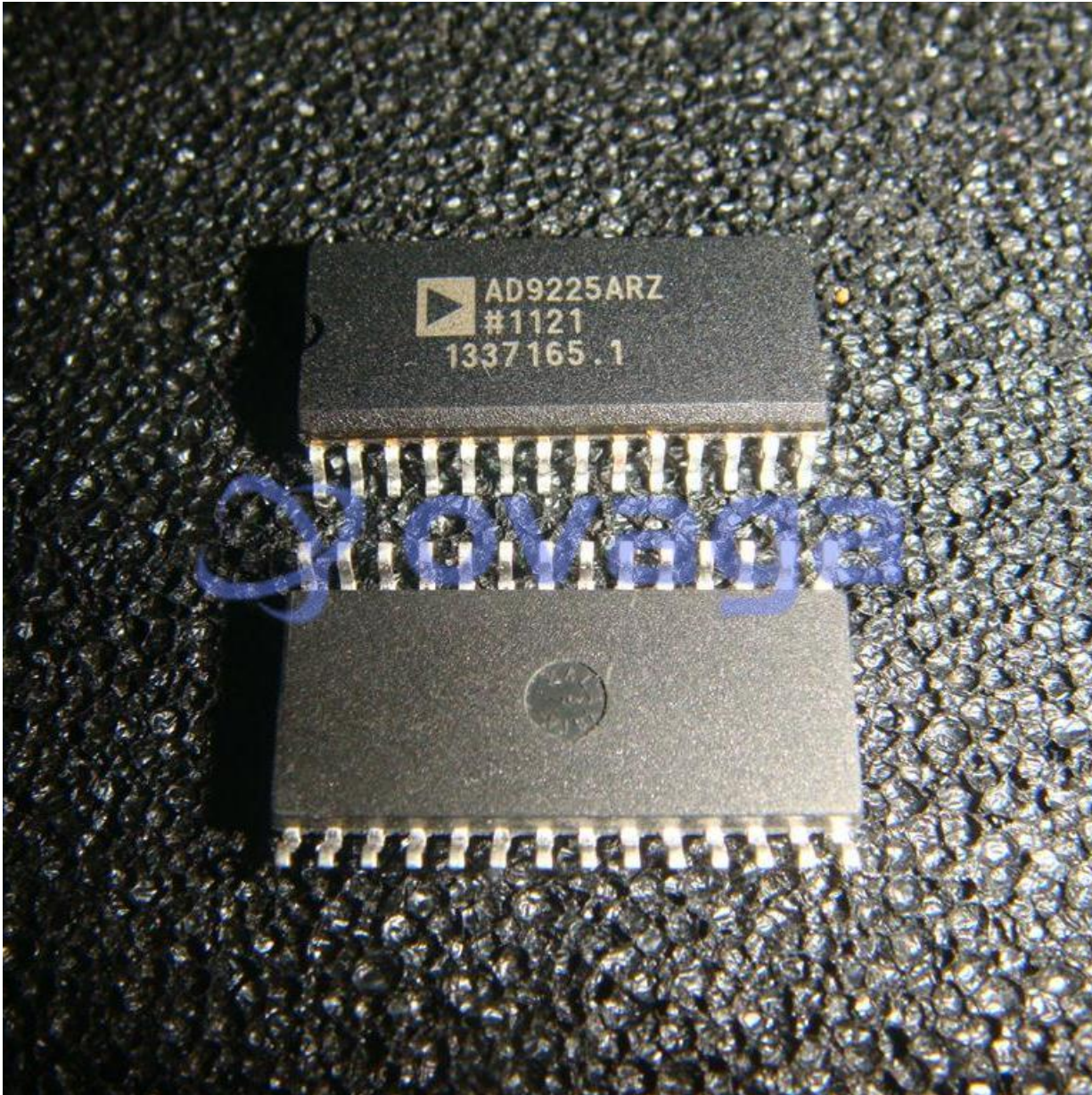
Single Supply—The AD9225 uses a single 5 V power supply, simplifying system power supply design. It also features a separate digital driven supply line to accommodate 3 V and 5 V logic families.

Pin Compatibility—The AD9225 is pin compatible with the AD9220, AD9221, AD9223, and AD9224 ADCs.

Features

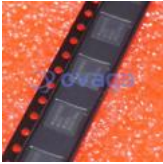
GENERAL DESCRIPTION The AD9225 is a monolithic, single-supply, 12-bit, 25 MSPS analog-to-digital converter with an on-chip, high performance sample-and-hold amplifier and voltage reference. The AD9225 uses a multistage differential pipelined architecture with output error correction logic to provide 12-bit accuracy at 25 MSPS data rates, and guarantees no missing codes over the full operating temperature range.

Monolithic 12-Bit, 25 MSPS ADC Low Power Dissipation: 280 mW Single 5 V Supply No Missing Codes Guaranteed
Differential Nonlinearity Error: ± 0.4 LSB Complete On-Chip Sample-and-Hold Amplifier and Voltage Reference Signal-to-Noise and Distortion Ratio: 71 dB Spurious-Free Dynamic Range: 85 dB Out-of-Range Indicator Straight Binary Output Data 28-Lead SOIC
28-Lead SSOP Compatible with 3 V Logic

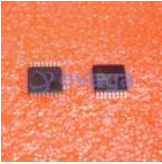




Related Products



[ADAS3022BCPZ](#)
Analog Devices, Inc
LFCSP-40



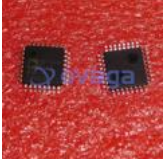
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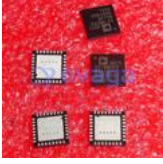
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