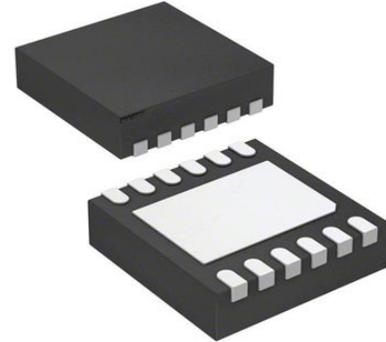


LDO Regulator Pos 1.2V to 2.3V 2A 10-Pin LFCSP EP T/R

Manufacturers	<a href="#">Analog Devices, Inc</a>
Package/Case	10-WDFN, CSP
Product Type	Power Management ICs
RoHS	Pb-free Halide free
Lifecycle	



Images are for reference only

Please submit RFQ for ADP7159ACPZ-04-R7 or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

[RFQ](#)

## General Description

The ADP7159 is an adjustable linear regulator that operates from 2.3 V to 5.5 V and provides up to 2 A of output current. Output voltages from 1.2 V to 3.3 V are possible depending on the model. Using an advanced proprietary architecture, the device provides high power supply rejection and ultralow noise, achieving excellent line and load transient response with only a 10  $\mu$ F ceramic output capacitor.

The ADP7159 is available in four models that optimize power dissipation and PSRR performance as a function of the input and output voltage.

The typical output noise of the ADP7159 regulator is 0.9  $\mu$ V rms from 100 Hz to 100 kHz and 1.7 nV/ $\sqrt{\text{Hz}}$  for noise spectral density from 10 kHz to 1 MHz. The ADP7159 is available in 10-lead, 3 mm  $\times$  3 mm LFCSP and 8-lead SOIC packages, making it not only a very compact solution, but also providing excellent thermal performance for applications requiring up to 2 A of output current in a small, low profile footprint.

## Features

Input voltage range: 2.3 V to 5.5 V

Adjustable output voltage range (VOUT):  
1.2 V to 3.3 V

Maximum load current: 2 A

Low noise

0.9  $\mu$ V rms total integrated noise from 100  
Hz to 100 kHz

1.6  $\mu$ V rms total integrated noise from 10  
Hz to 100 kHz

Noise spectral density: 1.7 nV/ $\sqrt{\text{Hz}}$  from  
10 kHz to 1 MHz

Power supply rejection ratio (PSRR)

68 dB from 1 kHz to 100 kHz

45 dB at 1 MHz

Dropout voltage: 200 mV typical at = 3.3  
V

Initial accuracy:  $\pm 0.6\%$  at  $>$

Accuracy over line, load, and temperature:  
 $\pm 1.5\%$

See data sheet for additional features

## Application

Regulation to noise sensitive applications: phase-locked loops (PLLs), voltage controlled oscillators (VCOs), and PLLs with integrated VCOs

Communications and infrastructure

Backhaul and microwave links

## Related Products



### [ADP3336ARMZ-REEL7](#)

Analog Devices, Inc  
MSOP-8



### [AD737JRZ](#)

Analog Devices, Inc  
SOP-8



### [ADP3367ARZ](#)

Analog Devices, Inc  
SOIC-8



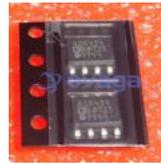
### [AD636JH](#)

Analog Devices, Inc  
TO-100-10



[ADP3330ARTZ.3-RL7](#)

Analog Devices, Inc  
SOT-23-6



[ADR434BRZ](#)

Analog Devices, Inc  
SOIC-8



[ADR421ARZ](#)

Analog Devices, Inc  
SOP-8



[ADR3412ARJZ-R7](#)

Analog Devices, Inc  
SOT-23-6