

CPLD XC9500XL Family 6.4K Gates 288 Macro Cells 208.3MHz 0.35um (CMOS) Technology 3.3V

Manufacturers	AMD Xilinx, Inc
Package/Case	BGA-256
Product Type	Programmable Logic ICs
RoHS	
Lifecycle	



Images are for reference only

Please submit RFQ for XC95288XL-6BGG256C or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

[RFQ](#)

General Description

XC95288XL-6BGG256C is a field-programmable gate array (FPGA) manufactured by Xilinx. Here are some of its features:

Features

- It has 288 I/O pins and 256 logic cells.
- It has a maximum operating frequency of 227 MHz.
- It has 18,432 bits of block RAM and 11,520 bits of distributed RAM.
- It has 8 digital clock managers (DCMs) for clock generation and management.
- It supports a variety of input/output standards, such as LVCMOS, LVTTTL, PCI, and LVDS.

Application

- Video and image processing
- Industrial automation and control
- Test and measurement equipment
- Data communication and networking
- Aerospace and defense systems

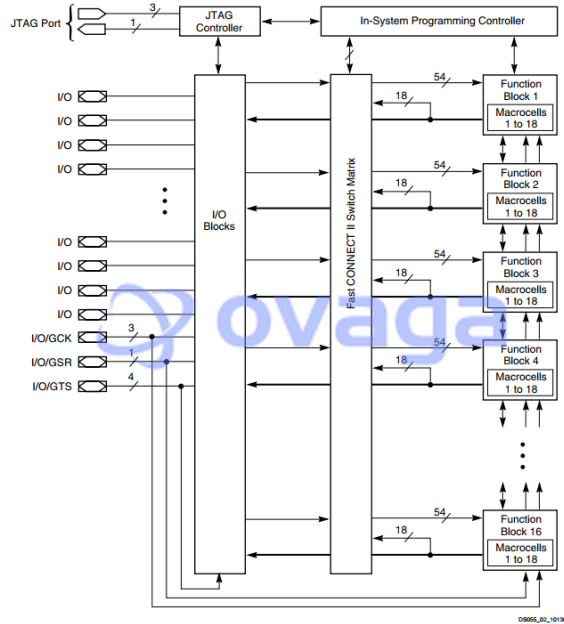
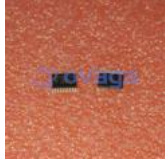


Figure 2: XC95288XL Architecture
Function Block outputs (indicated by the bold line) drive the I/O Blocks directly.

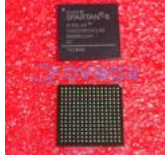
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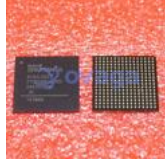
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