

Spartan-III 1.8V FPGA Family

Manufacturers	AMD Xilinx, Inc
Package/Case	TQFP144
Product Type	Programmable Logic ICs
RoHS	
Lifecycle	



Images are for reference only

Please submit RFQ for XC2S50E-6TQ144I or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

RFQ

General Description

XC2S50E-6TQ144I is a product code for a field-programmable gate array (FPGA) manufactured by Xilinx, a leading supplier of programmable logic devices. Here are some of its features:

Features

- The XC2S50E-6TQ144I has a logic cell density of 3,840 cells and 50,000 system gates.
- It has 36 I/O pads, which can be configured as input, output, or bidirectional.
- The FPGA supports various I/O standards, such as LVTTTL, LVCMOS, HSTL, SSTL, and LVDS.
- The device operates on a 1.8V core voltage and can tolerate up to 3.6V of I/O voltage.
- The FPGA has six digital clock managers (DCMs) that can generate and phase-shift clocks.

Application

- Digital signal processing (DSP)
- Image processing
- Network processing
- Control systems
- Industrial automation
- Communication systems

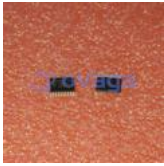


Related Products



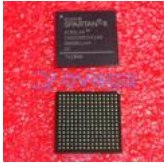
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AMD Xilinx, Inc
SOP-20



[XCF04SV0G20C](#)

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TSSOP20



[XC6SLX4-2CSG225C](#)

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BGA-225



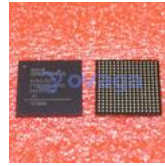
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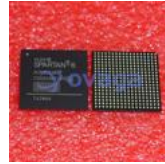
[XCF08PV0G48C](#)

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