

CPLD MAX 7000A Family 1.25K Gates 64 Macro Cells 100MHz CMOS Technology
3.3V 44Pin TQFP

Manufacturers	Altera Corporation (Intel)
Package/Case	TQFP-44
Product Type	Programmable Logic ICs
RoHS	RoHS
Lifecycle	



Images are for reference only

Please submit RFQ for EPM7064AETC44-10N or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

[RFQ](#)

General Description

EPM7064AETC44-10N is a programmable logic device (PLD) manufactured by Intel (formerly Altera), which is now a part of Intel's Programmable Solutions Group (PSG). It belongs to the MAX 7000 series of complex programmable logic devices (CPLDs).

Features

It has a total of 64 macrocells, which are programmable logic building blocks that can be configured to implement various digital logic functions.

It has 64 input/output (I/O) pins, which can be used for interfacing with other digital components or devices.

It operates at a speed grade of 10 nanoseconds (ns), meaning it can perform logic operations with a maximum frequency of 100 MHz.

It has a 3.3-volt (V) power supply requirement, which makes it compatible with many common digital logic interfaces.

It comes in a 44-pin thin quad flat pack (TQFP) package, which is a surface-mount package with a 0.8 mm pitch.

Application

EPM7064AETC44-10N can be used in a wide range of digital design applications, including but not limited to: communication systems, industrial control, automotive electronics, medical devices, and consumer electronics.

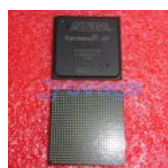
It can be used for implementing various digital logic functions such as combinational logic, sequential logic, and state machines.

It is commonly used for prototype development, as well as in production designs where programmable logic is required for flexible and reconfigurable digital logic functions.





Related Products



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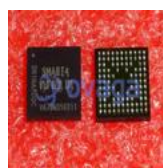
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