

16-bit Fixed-Point DSP, 160 MIPS, 160K bytes RAM; Package: 144 ld LQFP (20x20mm);  
No of Pins: 144; Temperature Range: Ind

Manufacturers	<a href="#">Analog Devices, Inc</a>
Package/Case	LQFP-144
Product Type	Embedded Processors & Controllers
RoHS	Pb-free Halide free
Lifecycle	



Images are for reference only

Please submit RFQ for ADSP-2191MBSTZ-140 or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

[RFQ](#)

## General Description

The ADSP-2191 is a 160 MIPS 16-bit fixed point DSP that is software compatible with DSPs in the ADSP-218x family, noted for its easy-to-use algebraic assembly syntax. Although the architectures are compatible, the ADSP-2191 architecture incorporates a number of enhancements that enables significant performance increases and improved C-compiler efficiency over its predecessor. The ADSP-2191 is available in a 144-lead LQFP and 144-ball mini-BGA package and is pin compatible with the low cost ADSP-2196 with 16K words RAM and 16K words ROM and the ADSP-2195 with 32K words RAM and 16K words ROM. Pin to pin-compatibility allows programmers a simple migration path to smaller memory models to reduce system cost without hardware redesign.

### Development Tools

The ADSP-2191 is supported by a complete set of software and hardware development tools including VisualDSP++™, evaluation boards, and JTAG emulators.

The VisualDSP++ integrated development and debug environment (IDDE) features an efficient C/C++ compiler, statistical profiling, and the new VisualDSP® Kernel (VDK) - all designed to make software development faster.

ADI makes it easier to evaluate the ADSP-2191 for a specific application with the ADSP-2191 EZ-KIT Lite™ evaluation system. The EZ-KIT Lite includes an ADSP-2191 DSP evaluation board and evaluation suite with VisualDSP++.

## Features

160 MIPS 16-bit fixed-point DSP

160K bytes of on-chip RAM, configured as 32K words 24-bit RAM and 32K words 16-bit RAM

16-bit parallel port interface supports SRAM, FLASH, or EPROM memory devices

Host port with DMA capability for glueless interface to 8- or 16-bit host processors

Three multichannel serial ports support A-law or  $\mu$ -law companding, and T1/E1/H100 telecommunications standard

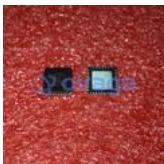
UART Port; Two SPI-compatible ports with slave select hardware

User selectable low-power operating modes

10x10mm BGA / 144 Lead LQFP Packages; -40°C to +85°C ambient temperature range



## Related Products



[ADUC7022BCPZ62](#)

Analog Devices, Inc  
LFCSP-40



[ADUC7020BCPZ62](#)

Analog Devices, Inc  
LFCSP-40



[ADUC841BSZ62-5](#)

Analog Devices, Inc  
QFP-52



[ADUC841BSZ62-3](#)

Analog Devices, Inc  
QFP-52



[ADUC831BSZ](#)

Analog Devices, Inc  
QFP-52



[ADSP-BF527BBCZ-5A](#)

Analog Devices, Inc  
BGA-208



[ADSP-21369BBPZ-2A](#)

Analog Devices, Inc  
SBGA-256



[ADSP-BF561SBBCZ-5A](#)

Analog Devices, Inc  
CSPBGA-256