

CPLD MAX® II Family 192 Macro Cells 201.1MHz 0.18um Technology 2.5V/3.3V 100-Pin TQFP Tray

Manufacturers	<a href="#">Altera Corporation (Intel)</a>
Package/Case	TQFP-100
Product Type	Programmable Logic ICs
RoHS	
Lifecycle	



Images are for reference only

Please submit RFQ for EPM240T100C5 or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

[RFQ](#)

## General Description

EPM240T100C5 is a type of programmable logic device (PLD) manufactured by Intel (formerly Altera).

## Features

The device has 240 macrocells, which can be configured as either combinatorial logic or as flip-flops for sequential logic.

It has 4,608 logic elements and 20,000 usable gates.

It operates at a maximum frequency of 312.5 MHz.

It uses a 1.8V core voltage and a 2.5V to 3.3V I/O voltage.

It has an on-chip oscillator and PLL for clock generation.

It supports configuration through JTAG, AS, or passive serial programming methods.

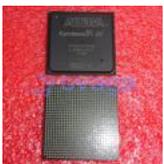
## Application

EPM240T100C5 can be used in a wide range of digital applications, including industrial automation, communication systems, test and measurement equipment, and consumer electronics.

It can be used to implement various functions, such as state machines, data path logic, and control logic.



## Related Products



### [EP4CE55F29C8N](#)

Altera Corporation (Intel)  
FBGA-780



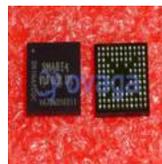
### [EPM1270T144A5N](#)

Altera Corporation (Intel)  
TQFP-144



### [EP2C35F672C8N](#)

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FBGA-672



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### [EPM7128AETC100-10](#)

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