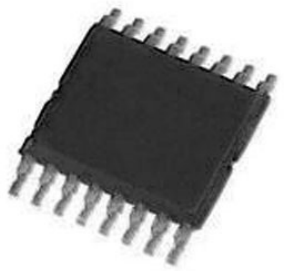
ADG1236YRUZ

Data Sheet

2 pF Off Cap, 1 pC Qinj ± 15/12 V Dual SPDT Switch; Package: TSSOP; No of Pins: 16; Temperature Range: Industrial

Manufacturers	Analog Devices, Inc
Package/Case	TSSOP-16
Product Type	Analog Switch ICs
RoHS	Rohs
Lifecycle	



Images are for reference only

Please submit RFQ for ADG1236YRUZ or Email to us: sales@ovaga.com We will contact you in 12 hours. RFQ

General Description

The ADG1236 is a monolithic CMOS device containing twoindependently selectable SPDT switches. It is designed on aniCMOS® process. iCMOS (industrial CMOS) is a modularmanufacturing process combining high voltage complementarymetal-oxide semiconductor (CMOS) and bipolar technologies.It enables the development of a wide range of high performanceanalog ICs capable of 33 V operation in a footprint that noprevious generation of high voltage devices has been able toachieve. Unlike analog ICs using conventional CMOS processes,iCMOS components can tolerate high supply voltages whileproviding increased performance, dramatically lower powerconsumption, and reduced package size.

The ultralow capacitance and charge injection of the devicemake it an ideal solution for data acquisition and sample-and-holdapplications, where low glitch and fast settling are required.Fast switching speed coupled with high signal bandwidth makesthe device suitable for video signal switching. iCMOS constructionensures ultralow power dissipation, making the device ideallysuited for portable and battery-powered instruments.

Each switch conducts equally well in both directions when onand has an input signal range that extends to the supplies. In theoff condition, signal levels up to the supplies are blocked. Bothswitches exhibit break-before-make switching action for use inmultiplexer applications.

Product Highlights

3 pF off capacitance (±15 V supply).

1 pC charge injection.

3 V logic-compatible digital inputs: = 0.8 V.

No VL logic power supply required.

Ultralow power dissipation: <0.03 µW.

16-lead TSSOP and 12-lead 3 mm × 3 mm LFCSP packages.

Features

1.3 pF off capacitance

3.5 pF on capacitance

1 pC charge injection

33 V supply range

120 Ω on resistance

Fully specified at +12 V, ±15 V

No VL supply required

3 V logic-compatible inputs

Rail-to-rail operation

16-lead TSSOP and 12-lead LFCSP packages

Typical power consumption: <0.03 µW

Application

Automatic test equipment

Data acquisition systems

Battery-powered systems

Sample-and-hold systems

Audio/video signal routing

Communication systems

Related Products

ADV7181CBSTZ

Analog Devices, Inc

LQFP-64

AD8170AR

Analog Devices, Inc

SOP8

AD724JR

Analog Devices, Inc

SOIC-16

ADV7393BCPZ

Analog Devices, Inc

LFCSP-VQ-40

ADV7391WBCPZ

Analog Devices, Inc

LFSCP-3

ADV7390BCPZ

Analog Devices, Inc

QFN32

ADV7341BSTZ

Analog Devices, Inc

LQFP-64

ADUM4160BRIZ

Analog Devices, Inc

SOIC-16