

HD1-6402B/883

Data Sheet

CMOS Universal Asynchronous Receiver Transmitter (UART)

Manufacturers	Renesas Technology Corp	
Package/Case	CDIP40	
Product Type	Integrated Circuits (ICs)	Muuuuuuuuu
RoHS		
Lifecycle		Images are for reference only

Please submit RFQ for HD1-6402B/883 or Email to us: sales@ovaga.com We will contact you in 12 hours.

<u>RFQ</u>

General Description

The HD-6402/883 is a CMOS UART for interfacing computers or microprocessors to an asynchronous serial data channel. The receiver converts serial start, data, parity and stop bits. The transmitter converts parallel data into serial form and automatically adds start, parity and stop bits. The data word length can be 5, 6, 7 or 8 bits. Parity may be odd or even. Parity checking and generation can be inhibited. The stop bits may be one or two or one and one-half when transmitting 5-bit code. The HD-6402/883 can be used in a wide range of applications including moderns, printers, peripherals and remote data acquisition systems. Utilizing the Intersil advanced scaled SAJI IV CMOS process permits operation clock frequencies up to 8.0MHz (500K Baud). Power requirements, by comparison, are reduced from 300mW to 10mW. Status logic increases flexibility and simplifies the user interface.

Features

This Circuit is Processed in Accordance to MIL-STD- 883 and is Fully Conformant Under the Provisions of Paragraph 1.2.1.

- 8.0MHz Operating Frequency (HD-6402/883B)
- 2.0MHz Operating Frequency (HD-6402/883R)
- Low Power CMOS Design
- Programmable Word Length, Stop Bits and Parity
- Automatic Data Formatting and Status Generation
- Compatible with Industry Standard UARTs
- Single +5V Power Supply
- CMOS/TTL Compatible Inputs



Related Products

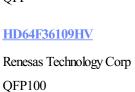


HD64F2368VTE34V

Renesas Technology Corp QFP120



HD64F2148AFA20V Renesas Technology Corp QFP









HD64F2638F20

Renesas Technology Corp QFP-128

HD64F3337YF16V

Renesas Technology Corp QFP80

HD6417729RF133BV

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HD64F2128FA20V

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HD6412340FA20V

Renesas Technology Corp QFP-100