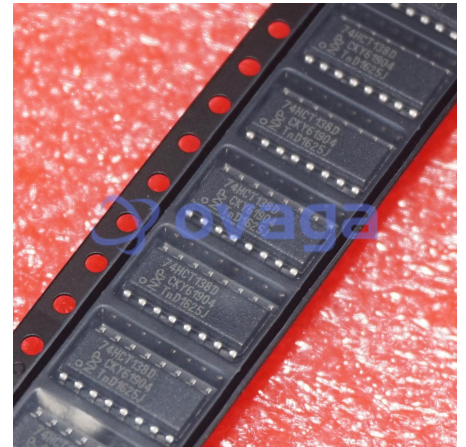


Decoder / Demultiplexer, HCT Family, 1 Gate, 3 Input, 8 Output, 4 mA, 4.5 V to 5.5 V, SOIC-16

Manufacturers	<u>NXP Semiconductor</u>
Package/Case	SOIC-16
Product Type	Logic ICs
RoHS	
Lifecycle	



Images are for reference only

Please submit RFQ for 74HCT138D or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

[RFQ](#)

General Description

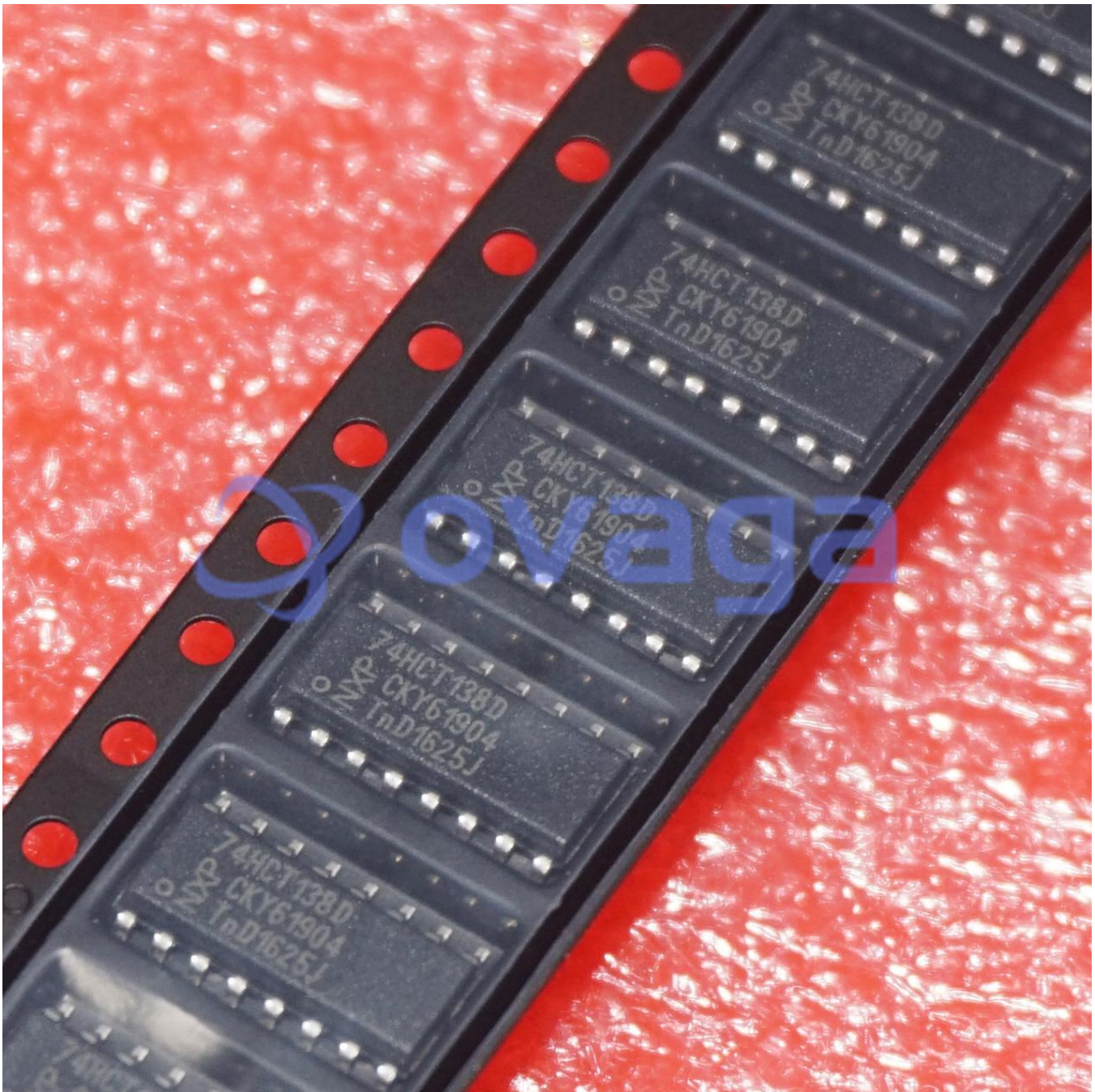
74HCT138D is a type of integrated circuit (IC) decoder/demultiplexer. It is a member of the 74HCT series of high-speed CMOS logic ICs, which are known for their low power consumption, high noise immunity, and compatibility with TTL inputs.

Features

- 3-to-8 line decoder/demultiplexer
- Schmitt-trigger inputs for noise immunity
- High-speed operation with typical propagation delay of 15ns
- Low power consumption
- Wide operating voltage range of 2V to 6V
- Output capability: standard

Application

- Address decoding in memory and microprocessor systems
- Data demultiplexing
- General-purpose logic decoding



Related Products



[74HC4050D](#)

NXP Semiconductor
16-SOIC



[74HC574D](#)

NXP Semiconductor
20-SOIC



[74HC132D](#)

NXP Semiconductor
SOP-14



[74HC165D](#)

NXP Semiconductor
SOP-16



[74HC259D](#)

NXP Semiconductor
SOP-16



[74HCT02D](#)

NXP Semiconductor
SOP-14



[74HC14D](#)

NXP Semiconductor
SOP-14



[74HC04D](#)

NXP Semiconductor
SOP-14