

CPLD MAX 7000A Family 2.5K Gates 128 Macro Cells 98MHz 3.3V 100-Pin TQFP Tray

Manufacturers	<u>Altera Corporation (Intel)</u>
Package/Case	TQFP-100
Product Type	Programmable Logic ICs
RoHS	
Lifecycle	



Images are for reference only

Please submit RFQ for EPM7128AETC100-10 or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

[RFQ](#)

## General Description

EPM7128AETC100-10 is a field-programmable gate array (FPGA) manufactured by Altera (now Intel). It is a member of the MAX 7000 family of FPGAs and has 128 macrocells (logic blocks) and 7,680 usable gates.

## Features

Maximum user gates: 7,680

Logic elements: 128 macrocells (each macrocell contains a flip-flop, a look-up table, and a programmable interconnect)

Maximum input/output pins: 100

Operating speed: 10ns

Operating voltage: 5V

Package: 100-pin TQFP

## Application

Digital signal processing

Control systems

Data processing and communications

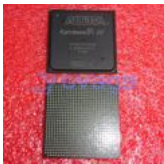
High-speed instrumentation

Robotics and automation

Video and image processing

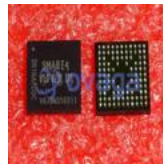


**Related Products**



[EP4CE55F29C8N](#)

Altera Corporation (Intel)  
FBGA-780



[EPM240M100C5N](#)

Altera Corporation (Intel)  
BGA-100



[EPM1270T144A5N](#)

Altera Corporation (Intel)  
TQFP-144



[EPM570F256C5N](#)

Altera Corporation (Intel)  
FBGA-256



[EP2C35F672C8N](#)

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FBGA-484



[EPM2210F256C4](#)

Altera Corporation (Intel)  
FBGA-256