

Spartan®-II Family 50K Gates 1728 Cells 263MHz 0.18um Technology 2.5V

Manufacturers	<a href="#">AMD Xilinx, Inc</a>
Package/Case	QFP208
Product Type	Programmable Logic ICs
RoHS	
Lifecycle	



Images are for reference only

Please submit RFQ for XC2S50-5PQG208C or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.

[RFQ](#)

## General Description

XC2S50-5PQG208C is a specific model of field-programmable gate array (FPGA) manufactured by Xilinx.

### Features

- 5V tolerant inputs
- 3.3V and 2.5V compliant I/Os
- 5760 logic cells
- 36 Kbits of Block RAM
- Maximum operating frequency of 233 MHz
- 50,000 system gates
- 2 global clock lines and up to 16 regional clock lines
- 208-pin plastic quad flat pack (PQFP) package

### Application

- Altera EP2C20F256C8N
- Lattice LC4256V-5TN100C
- Actel A3P1000-FG144I
- Atmel AT94S05AL-25BQC
- Cypress CYCLONE II EP2C20F484C7N



## Related Products



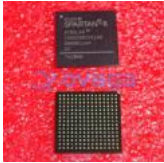
### [XC18V01S020C](#)

AMD Xilinx, Inc  
SOP-20



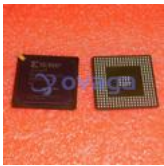
### [XCF04SV0G20C](#)

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### [XCV50-6BG256C](#)

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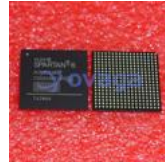
### [XCF08PV0G48C](#)

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