

ADN4667ARUZ

Data Sheet

LVDS Driver, Quad, Differential Line Driver, 8 mA, -40 °C, 85 °C, 3 V

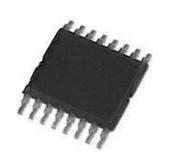
Manufacturers Analog Devices, Inc

Package/Case TSSOP-16

Product Type Interface ICs

RoHS Rohs

Lifecycle



Images are for reference only

Please submit RFQ for ADN4667ARUZ or Email to us: sales@ovaga.com We will contact you in 12 hours.

RFO

General Description

The device accepts low voltage TTL/CMOS logic signals and converts them to a differential current output of typically ± 3.1 mA for driving a transmission medium such as a twisted pair cable. The transmitted signal develops a differential voltage of typi- cally ± 310 mV across a termination resistor at the receiving end. This is converted back to a TTL/CMOS logic level by an LVDS receiver, such as the ADN4668.

The ADN4667 also offers active high and active low enable/ disable inputs (EN and EN). These inputs control all four drivers and turn off the current outputs in the disabled state to reduce the quiescent power consumption to typically 10 mW.

The ADN4667 and its companion LVDS receiver, the ADN4668, offer a new solution to high speed, point-to-point data trans- mission, and a low power alternative to emitter-coupled logic (ECL) or positive emitter-coupled logic (PECL).

Features Application

400 Mbps (200 MHz) switching rates Backplane data transmission

Flow-through pinout simplifies PCB layout Cable data transmission

300 ps typical differential skew Clock distribution

400 ps maximum differential skew Data Sheet, Rev. A, 5/08

1.7 ns maximum propagation delay

3.3 V power supply

See Data Sheet for Additional Information

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ADV7181CBSTZ

Analog Devices, Inc LQFP-64



<u>AD724JR</u>

Analog Devices, Inc SOIC-16



ADV7391WBCPZ

Analog Devices, Inc LFSCP-3



ADV7341BSTZ

Analog Devices, Inc LQFP-64



AD8170AR

Analog Devices, Inc SOP8



ADV7393BCPZ

Analog Devices, Inc LFCSP-VQ-40



ADV7390BCPZ

Analog Devices, Inc QFN32



ADUM4160BRIZ

Analog Devices, Inc SOIC-16