

2A Low-Voltage PMOS-NMOS Bridge Driver; Package: 8-SOIC; Container: Tape & Reel, MOSFET & Power Driver ICs Conn FPC Conn SKT 30 POS 1mm Solder ST

Manufacturers	ON Semiconductor, LLC
Package/Case	SOIC-8
Product Type	Power Management ICs
RoHS	Green
Lifecycle	



Images are for reference only

Please submit RFQ for FAN3268TMX or [Email to us: sales@ovaga.com](mailto:sales@ovaga.com) We will contact you in 12 hours.



General Description

The FAN3268 dual 2A gate driver is optimized to drive a high-side P-channel MOSFET and a low-side N-channel MOSFET in motor control applications operating from a voltage rail up to 18V. The driver has TTL input thresholds and provides buffer and level translation functions from logic inputs. Internal circuitry provides an under-voltage lockout function that prevents the output switching devices from operating if the VDD supply voltage is below the operating level. Internal 100kΩ resistors bias the non-inverting output low and the inverting output to VDD to keep the external MOSFETs off during startup intervals when logic control signals may not be present. The FAN3268 driver incorporates MillerDrive™ architecture for the final output stage. This bipolar-MOSFET combination provides high current during the Miller plateau stage of the MOSFET turn-on / turn-off process to minimize switching loss, while providing rail-to-rail voltage swing and reverse current capability. The FAN3268 has two independent enable pins that default to on if not connected. If the enable pin for non-inverting channel A is pulled low, OUTA is forced low; if the enable pin for inverting channel B is pulled low, OUTB is forced high. If an input is left unconnected, internal resistors bias the inputs such that the external MOSFETs are off.

Application

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Related Products



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SOIC-8



[FAN7602CMX](#)

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WLCSP-16